

S133AR400

LPDDR3 256 Ball x2

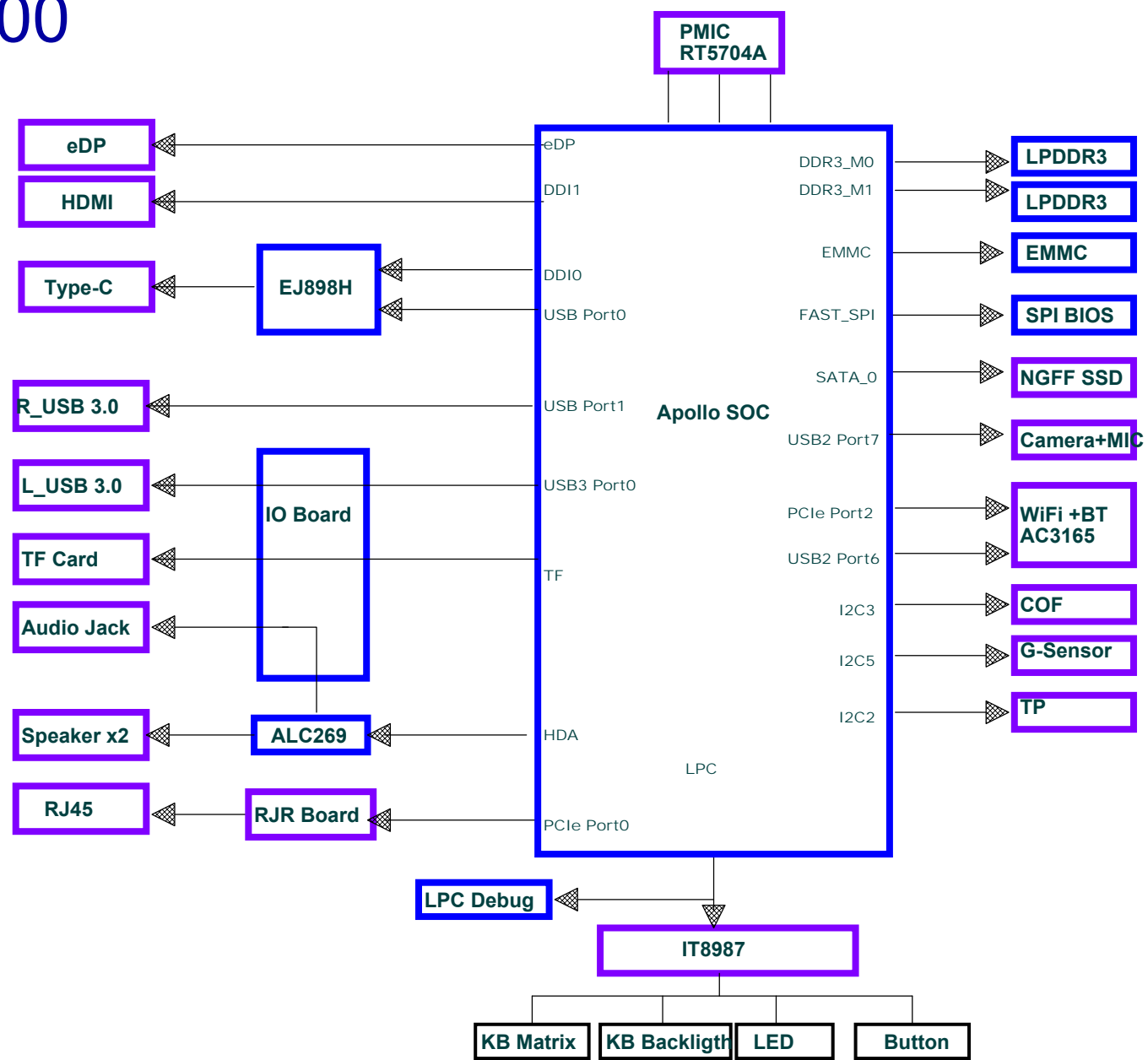


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40	NC

[illegible]

I2C TABLE


INTERFACE	ISH-I2C0	ISH-I2C1	ISH-I2C2	I2C-0	I2C-1	I2C-2	I2C-3	I2C-4
TOUCH CONTROLLER					0X10			
ACCELROMETER	0X18							
GYRO	0X68							
ALS	0X10							
SAR PROX	0X28							
FRONT CAMERA						0X10		
REAR CAMERA SENSOR						0X36		
REAR CAMERA VCM						0X0C		
WWAN-GNSS			TBD					
Wireless Charging				TBD				

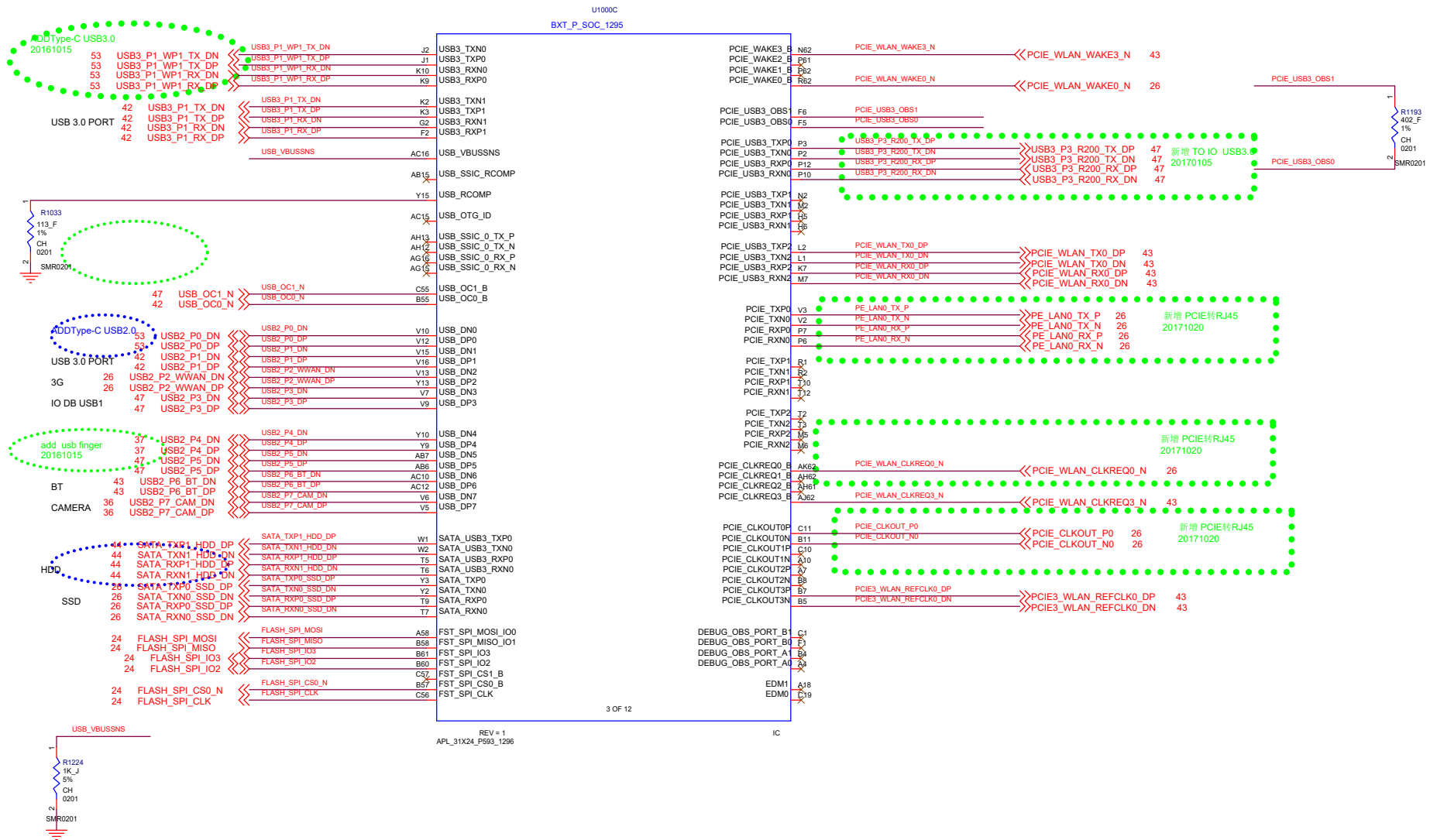
EC I2C INTERFACE	EC_12C-0
TYPE-C PD CONTROLLER	0XC0
BATTERY CHARGER	TBD
EEPROM	0XA0

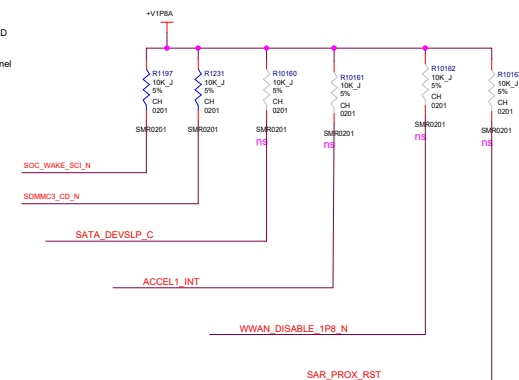
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Custom	TABLE OF CONTENTS			R100
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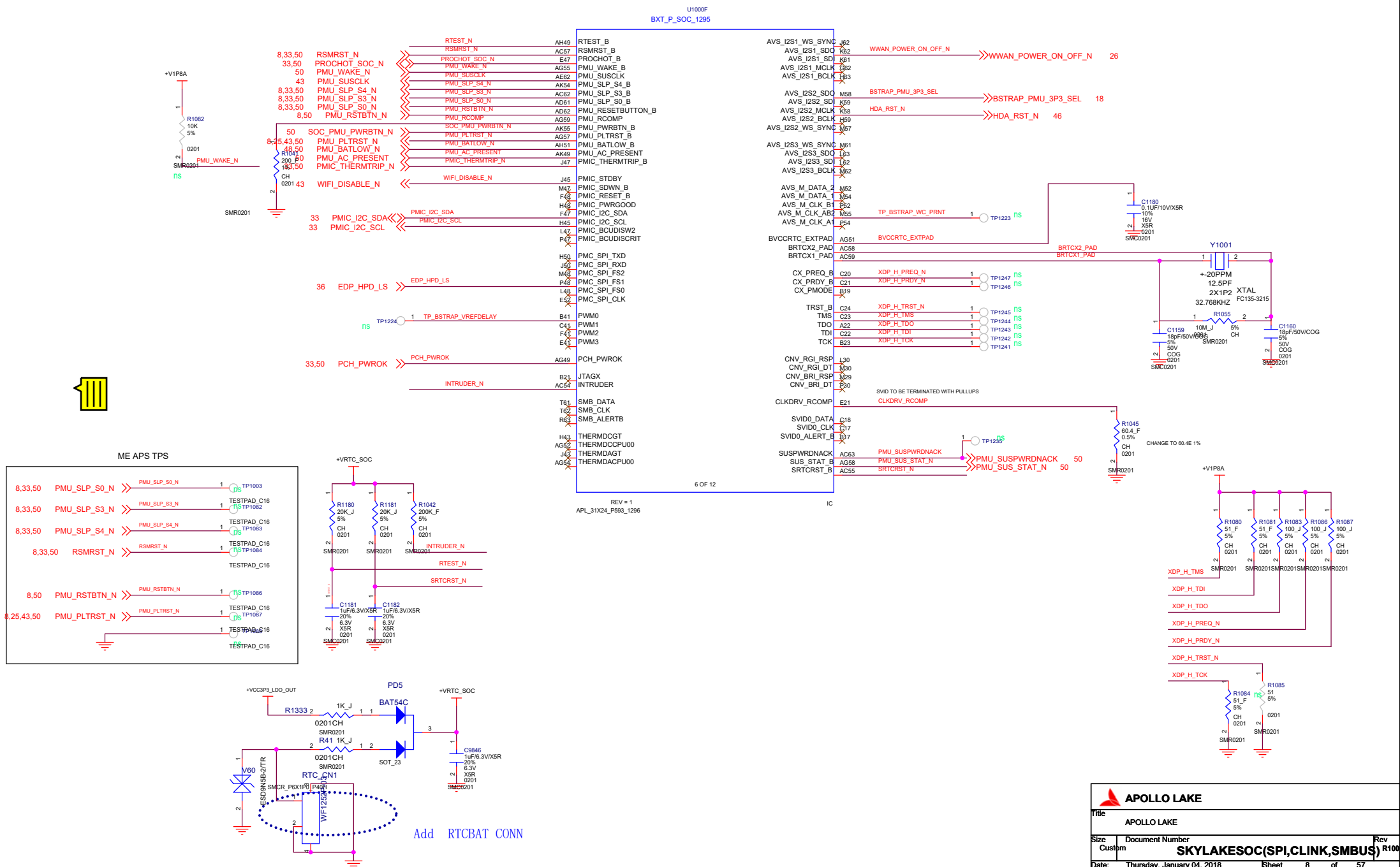


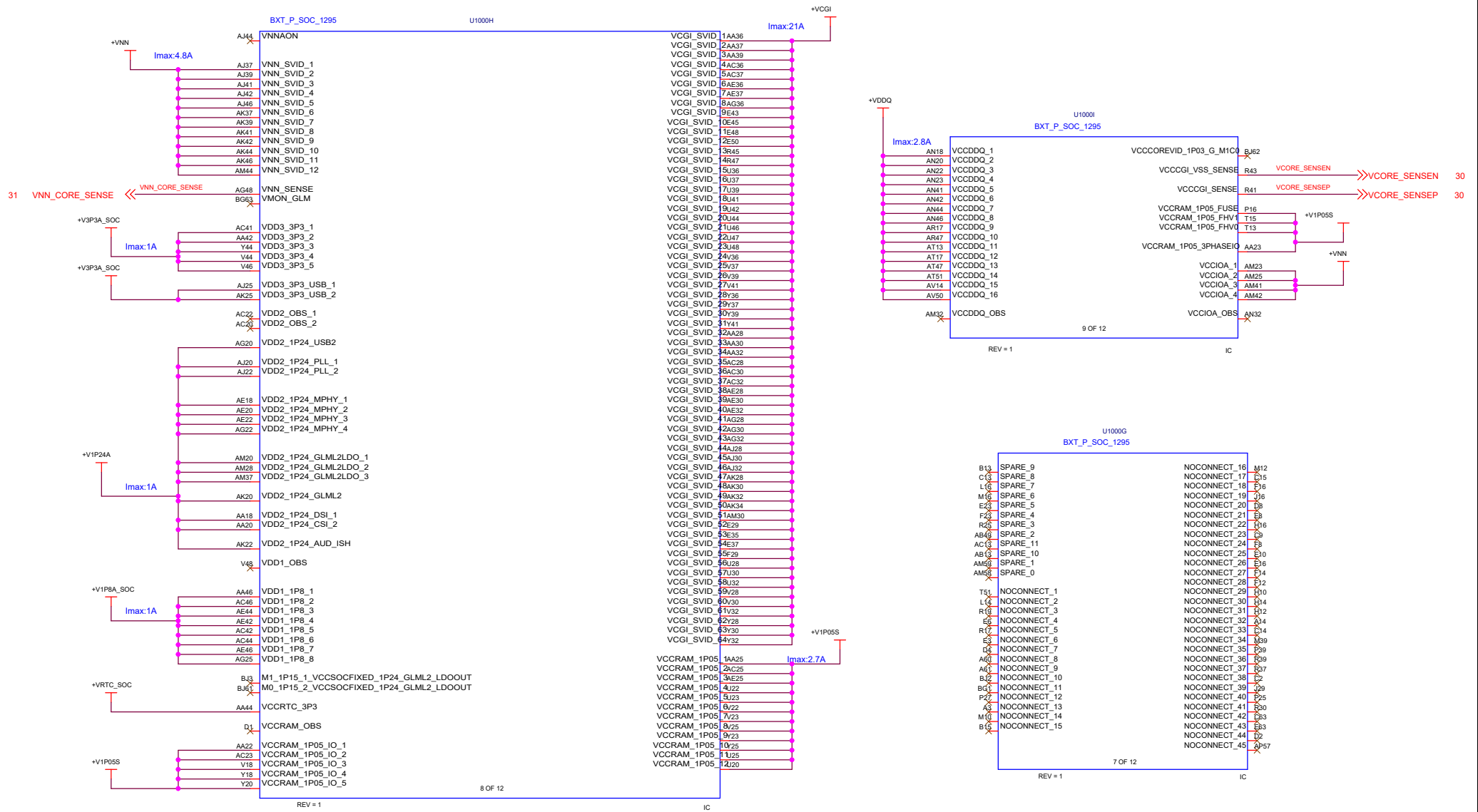
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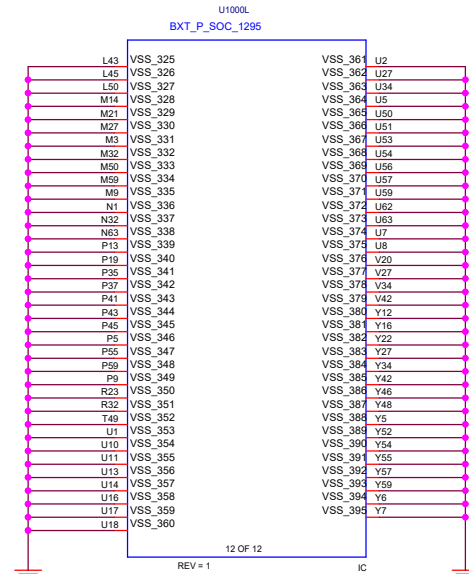
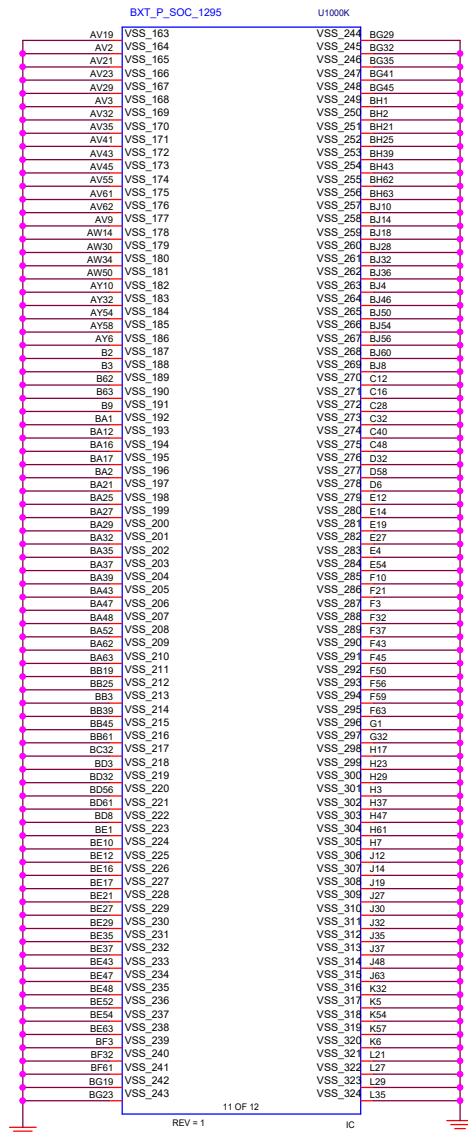
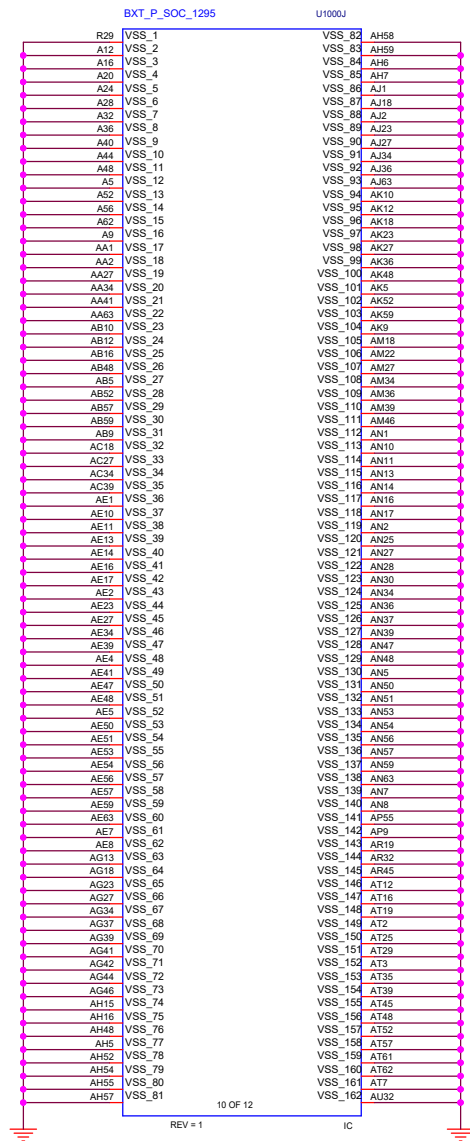
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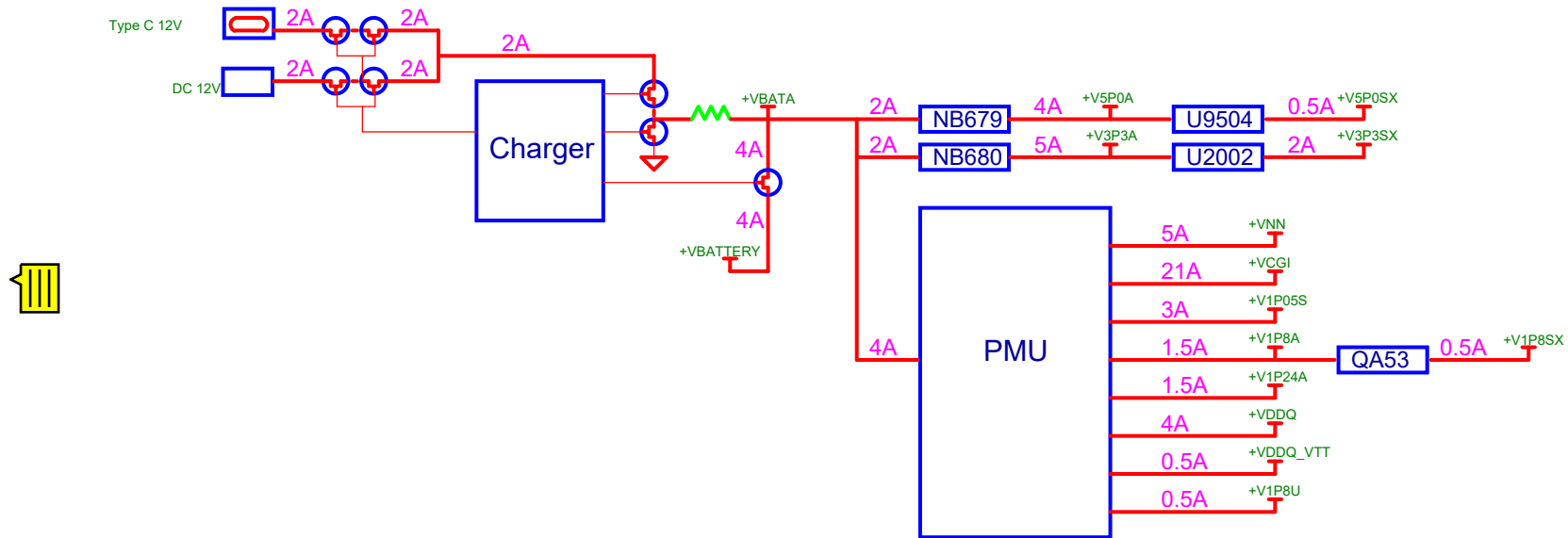










Power Deliver




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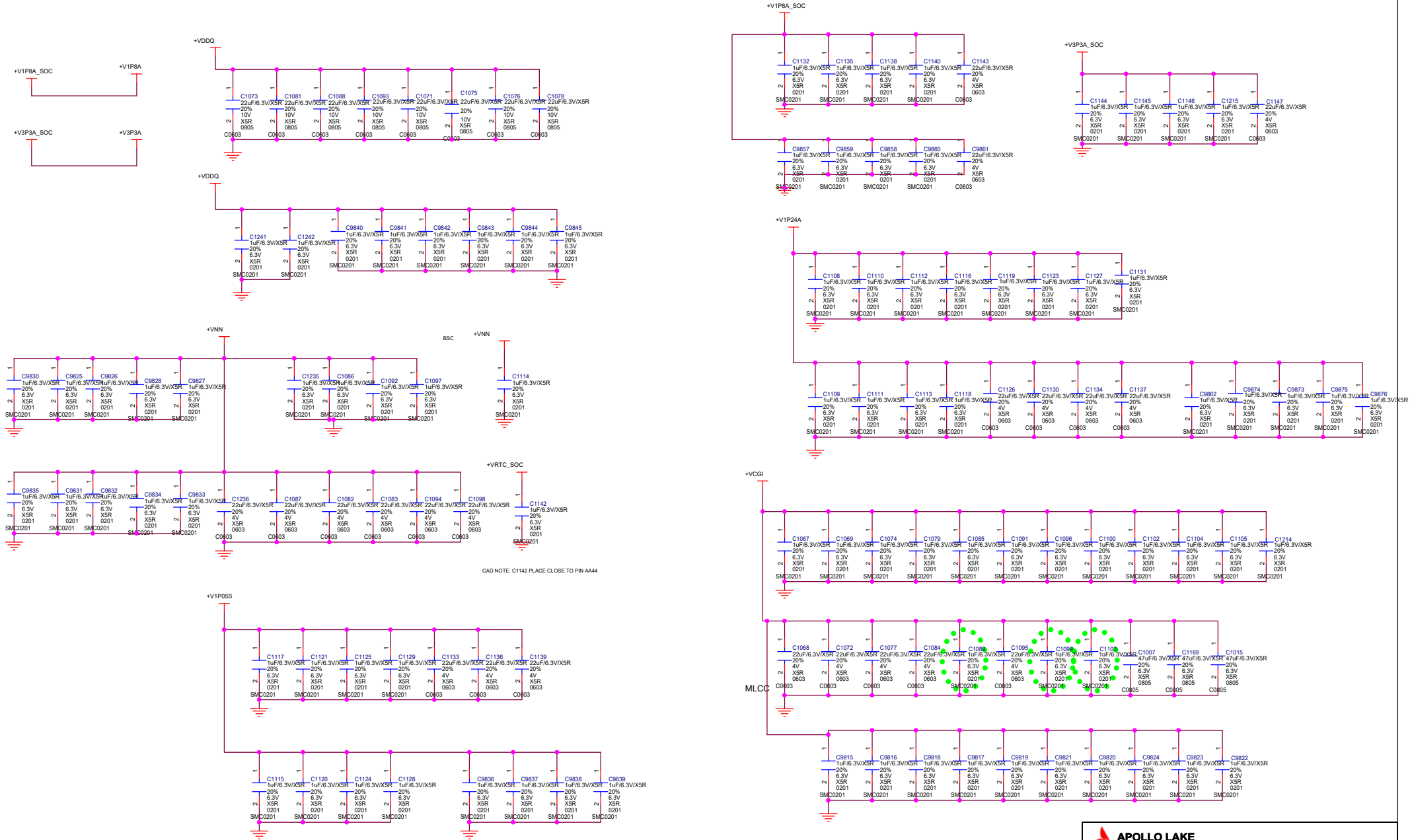
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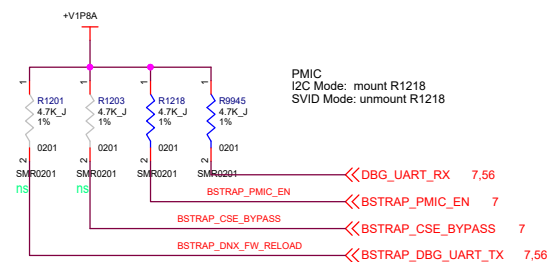
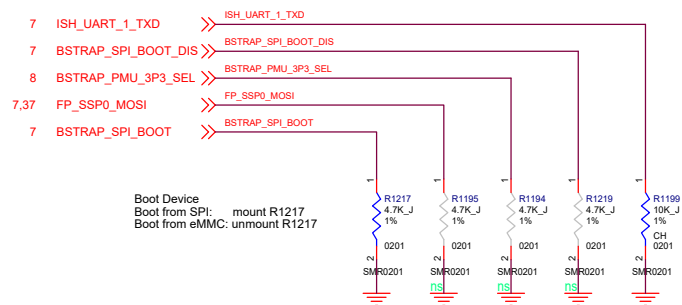
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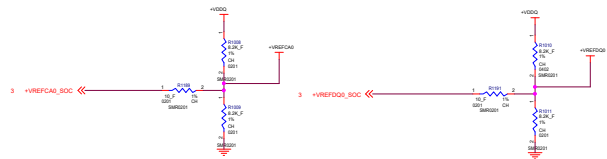
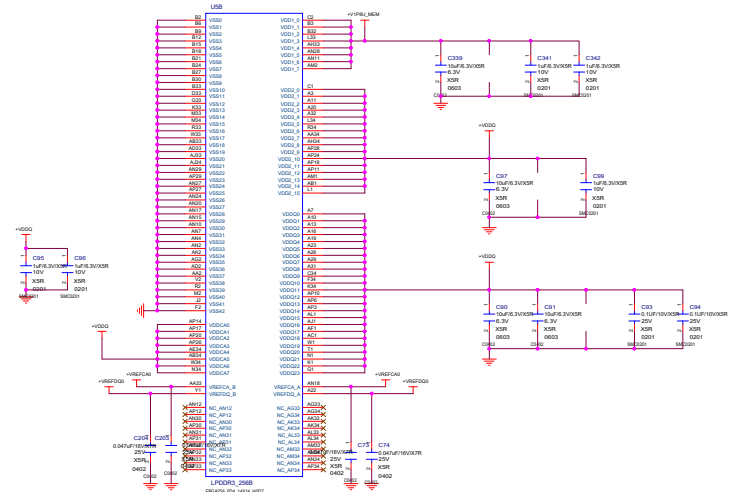
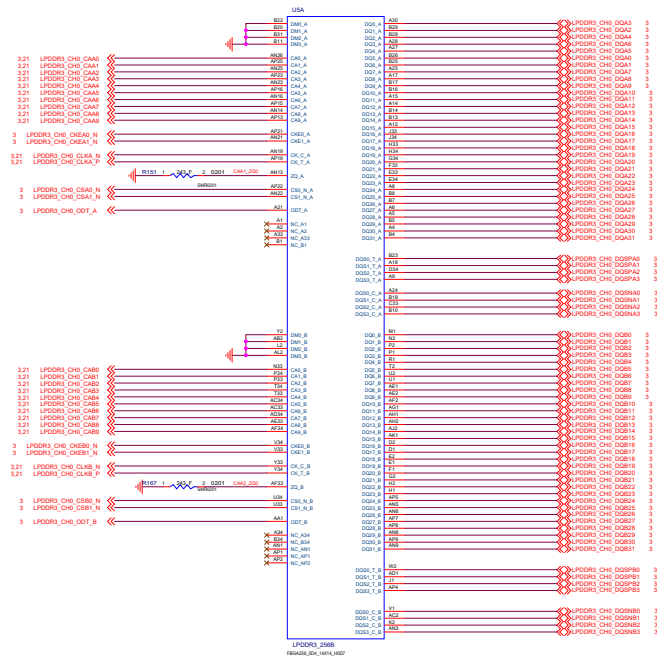
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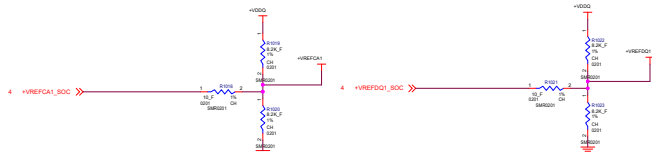
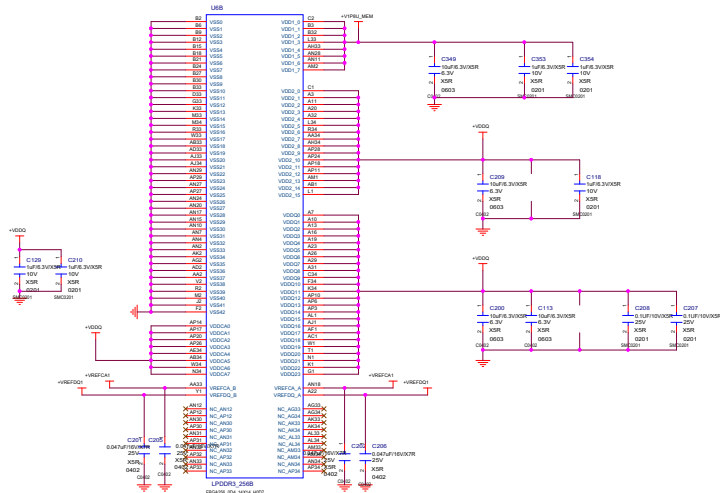
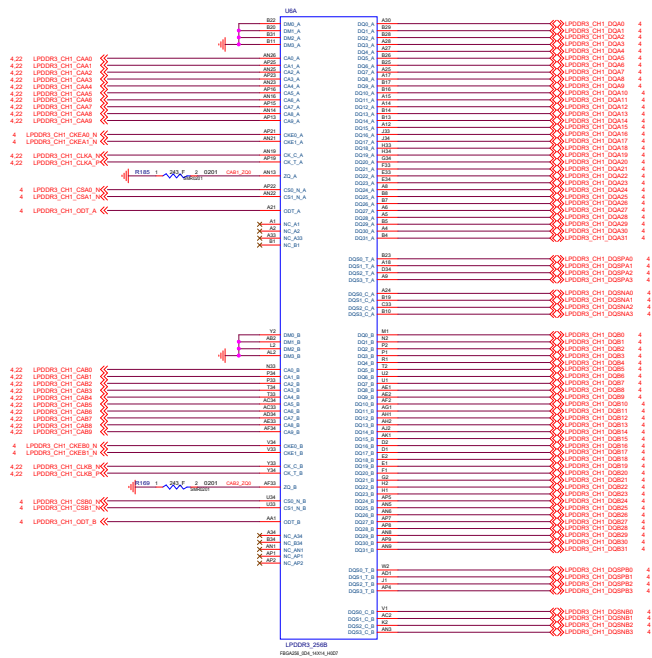
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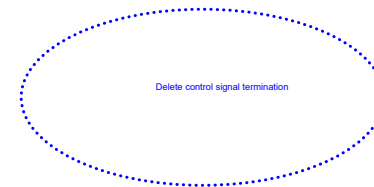
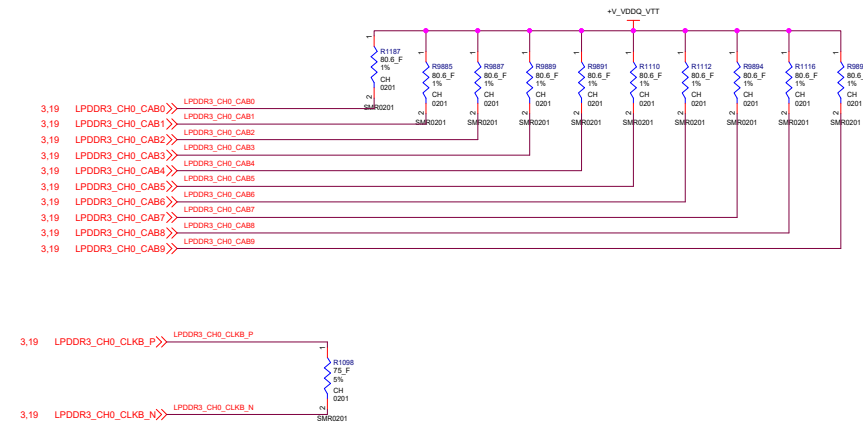
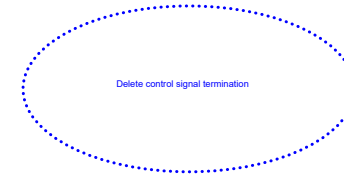
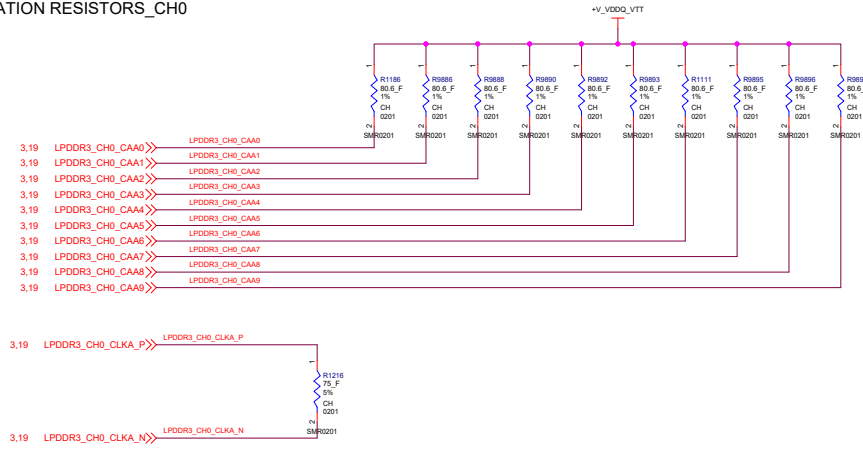




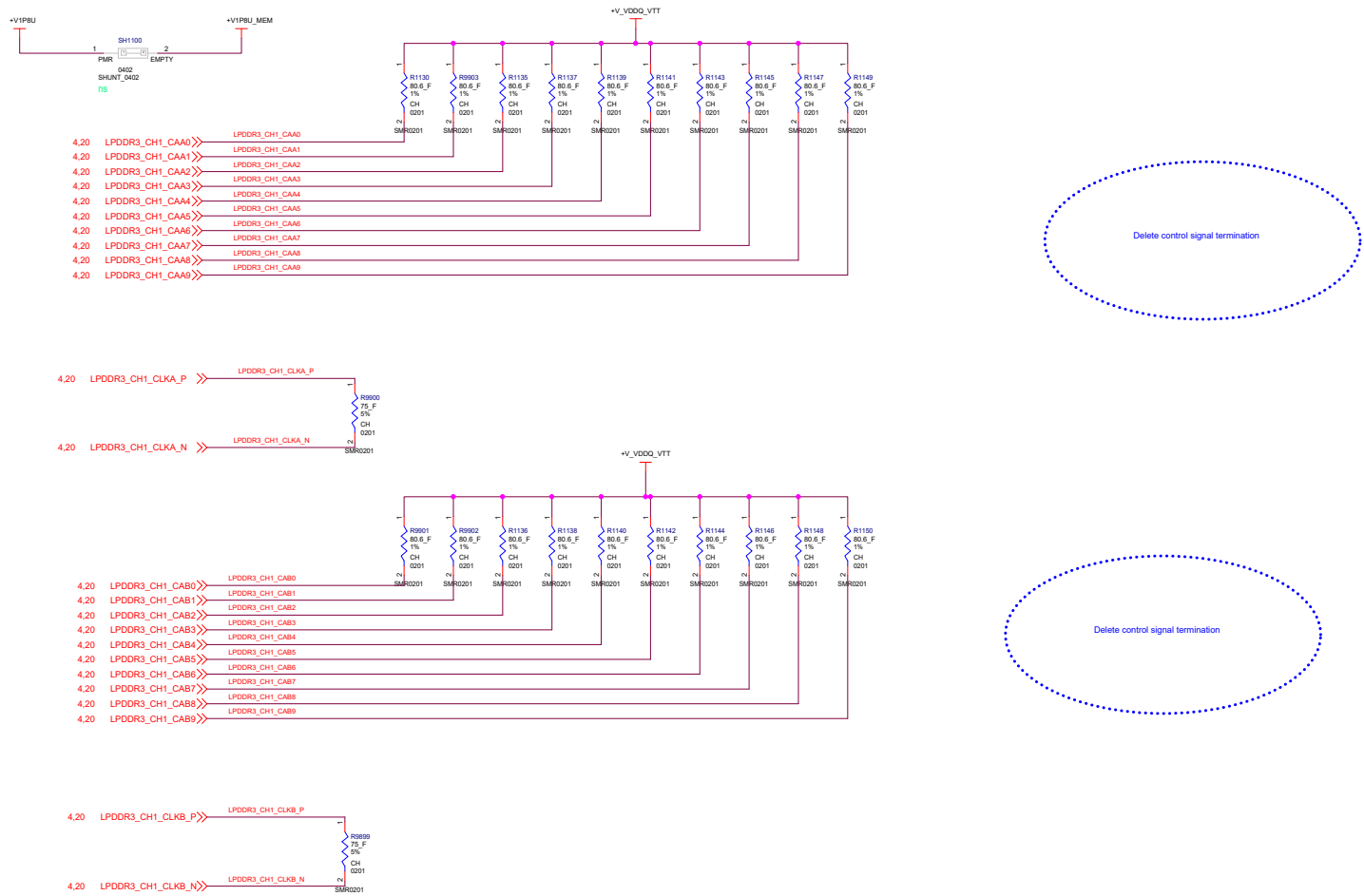


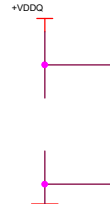
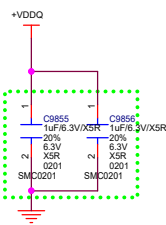
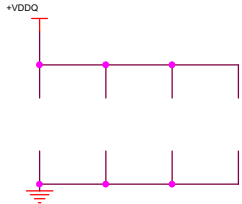
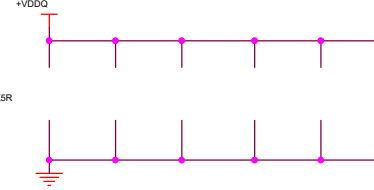
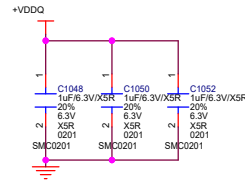
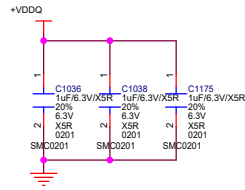
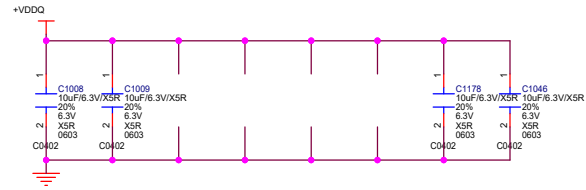
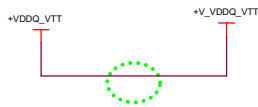


LPDDR3L-RS TERMINATION RESISTORS_CH0



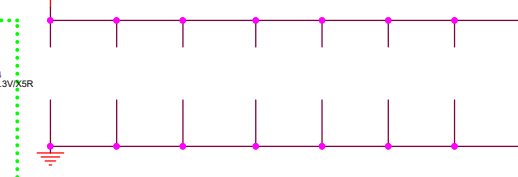
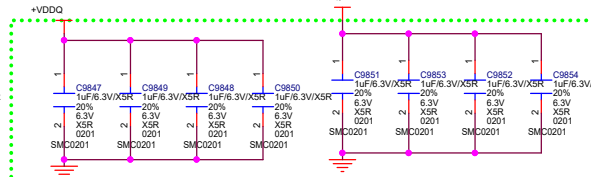
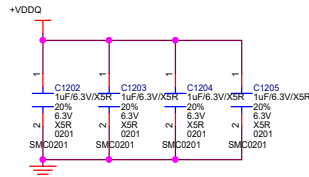
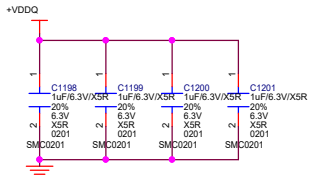
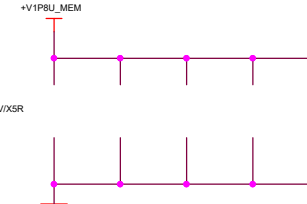
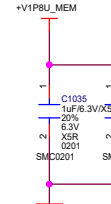
LPDDR3L-RS TERMINATION RESISTORS_CH1





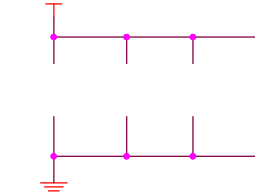
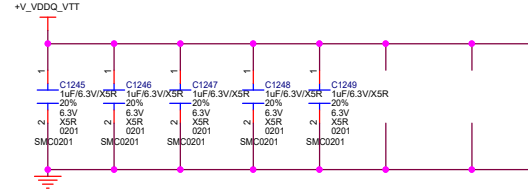
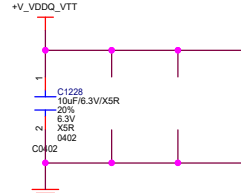
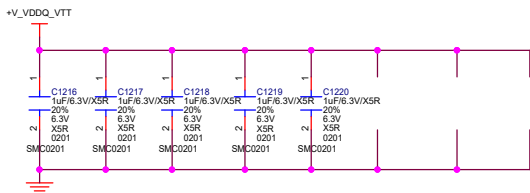
FOUR 0402 IN MID CHANNEL

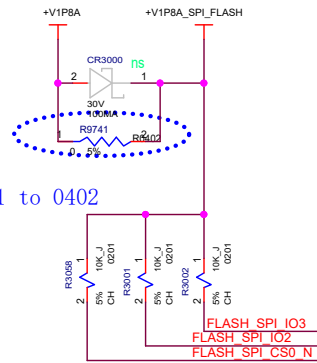
TWO 0402 ON EACH END CHANNEL



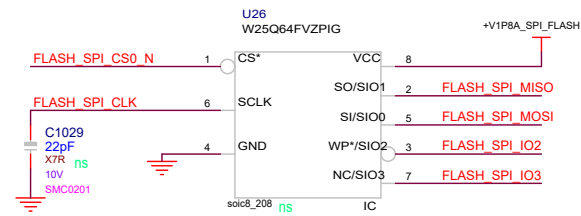
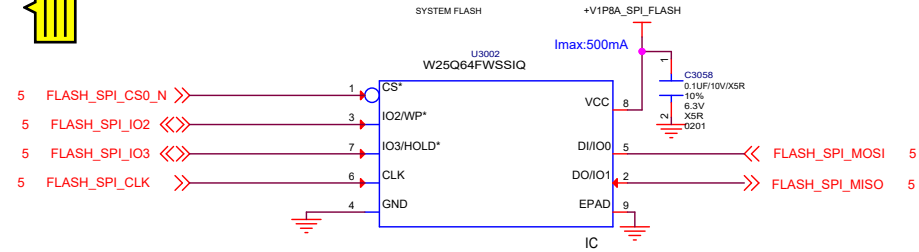
NOTE: PLACE ON TOP - 4 PER DEVICE


NOTE: PLACE ON TOP - 4 PER DEVICE

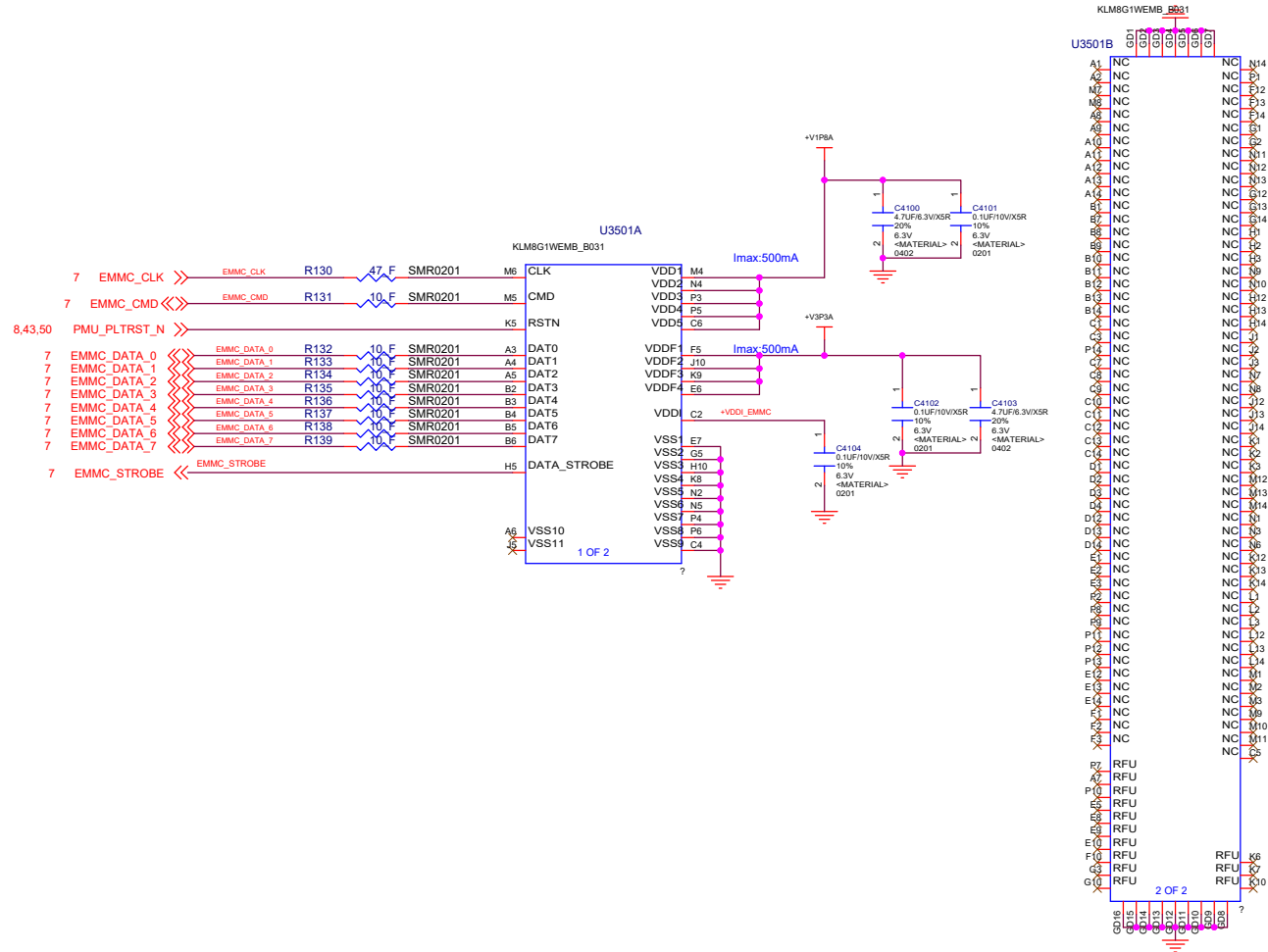


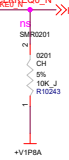
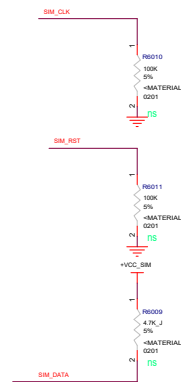



Change Footprint From 0201 to 0402

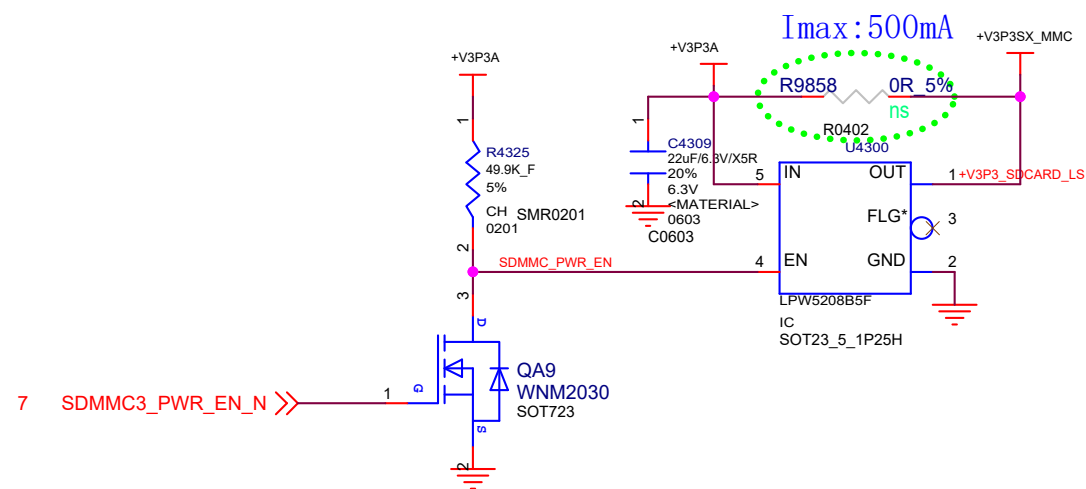



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Size B	Document Number LPDDR3 DECOUPLING		Rev R100
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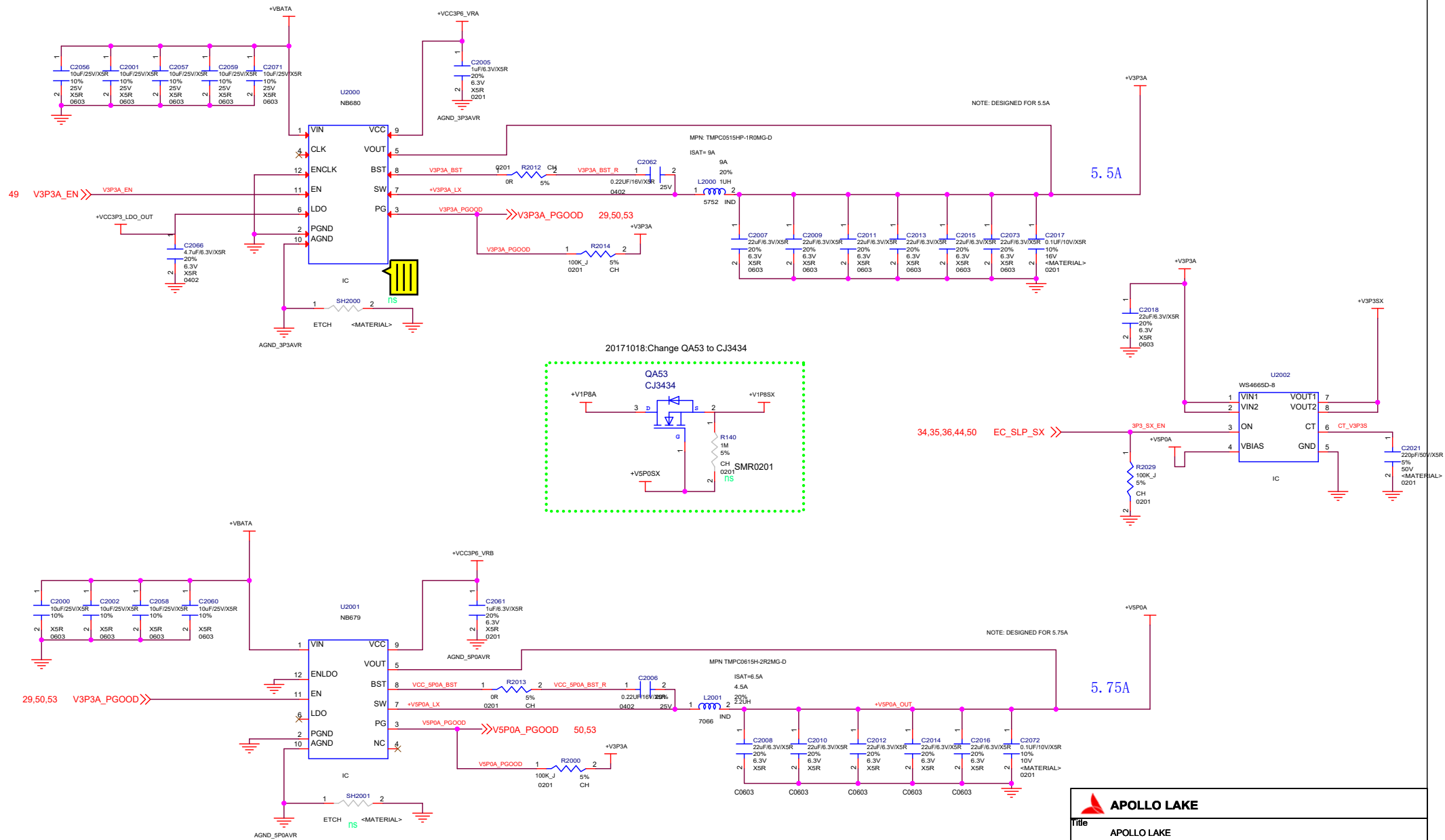





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Size B	Document Number MICRO-SD CARD	Rev R100
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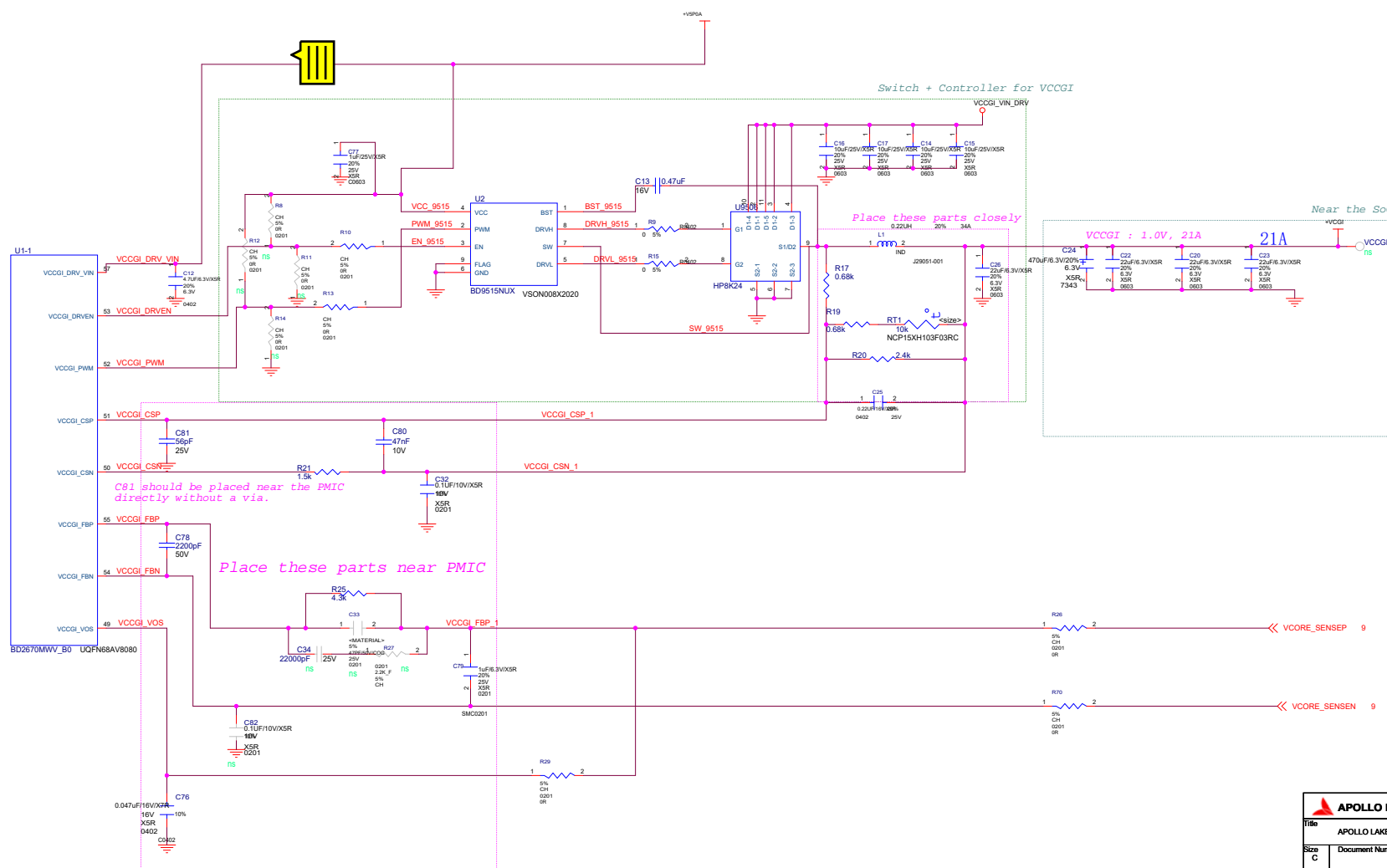
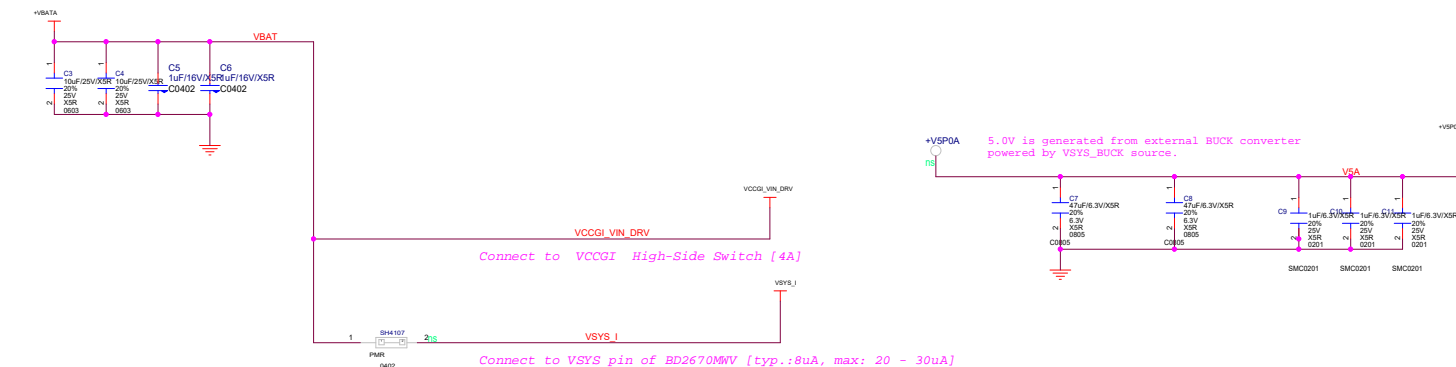


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Size A	Document Number SDCARD POWER		Rev R100
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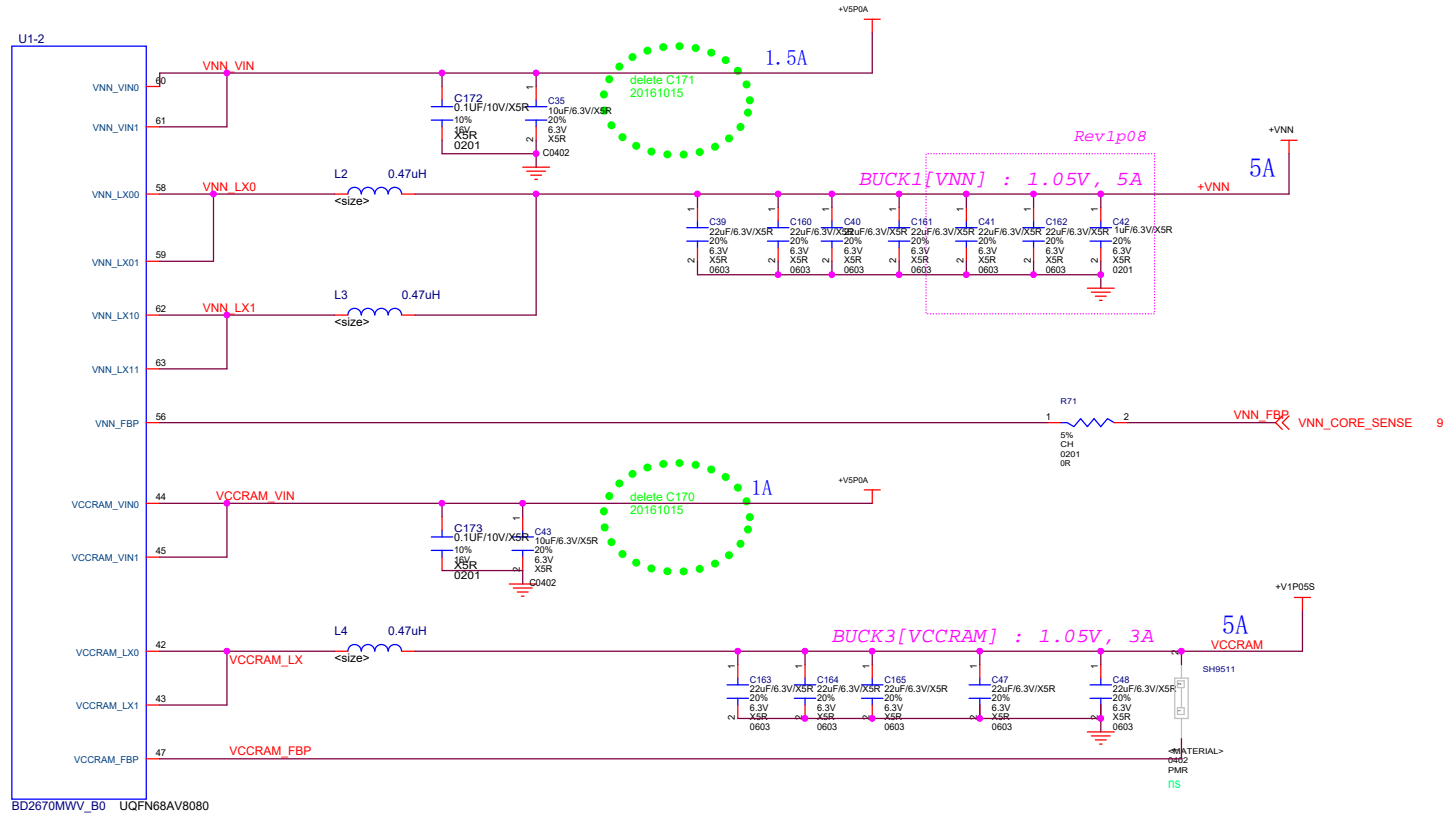


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Size Custom	Document Number POWER DELIVERY 5V AND 3.3V		Rev R100
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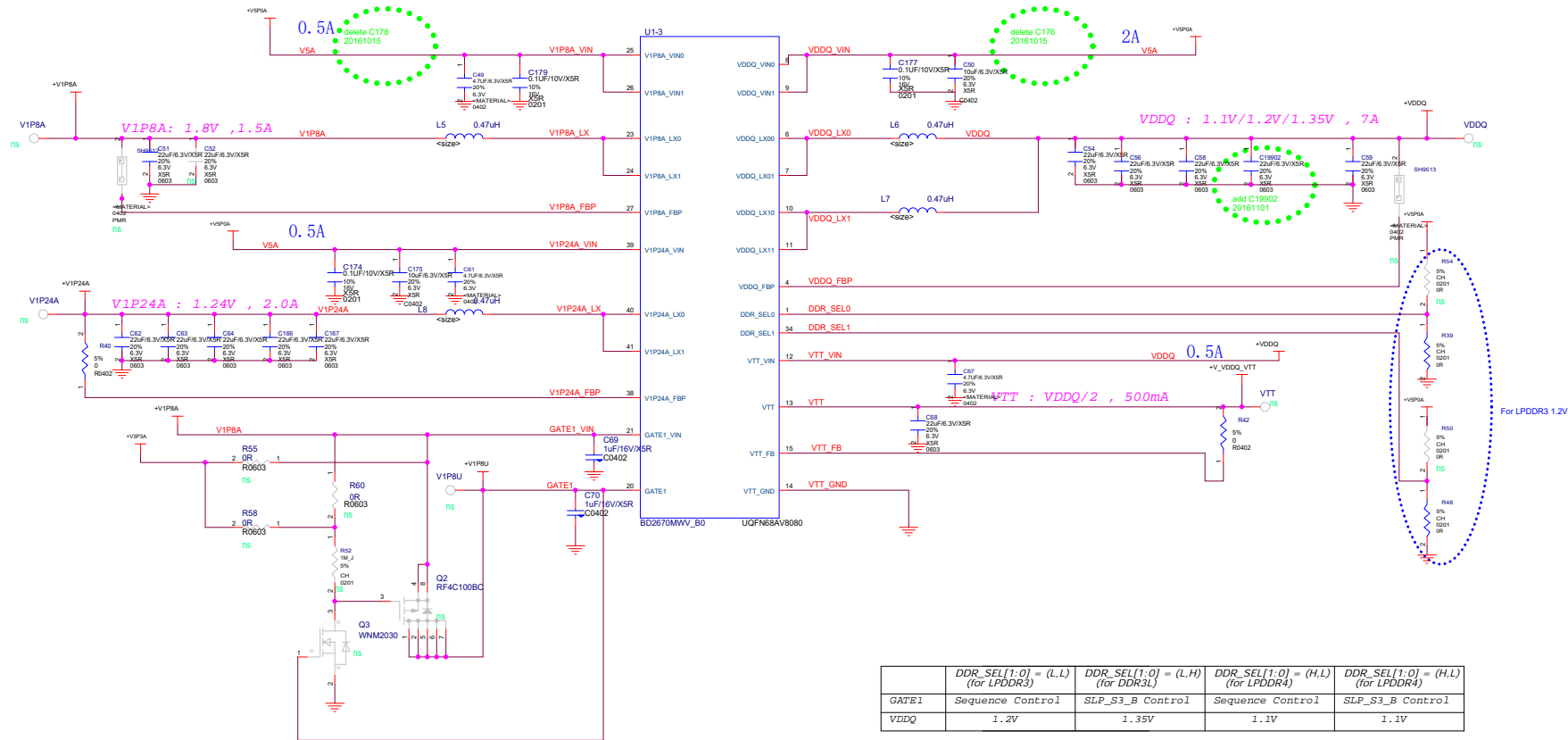
2S/3S battery input [5.4V to 21V]



VNN[BUCK1], VCCRAM[BUCK3]

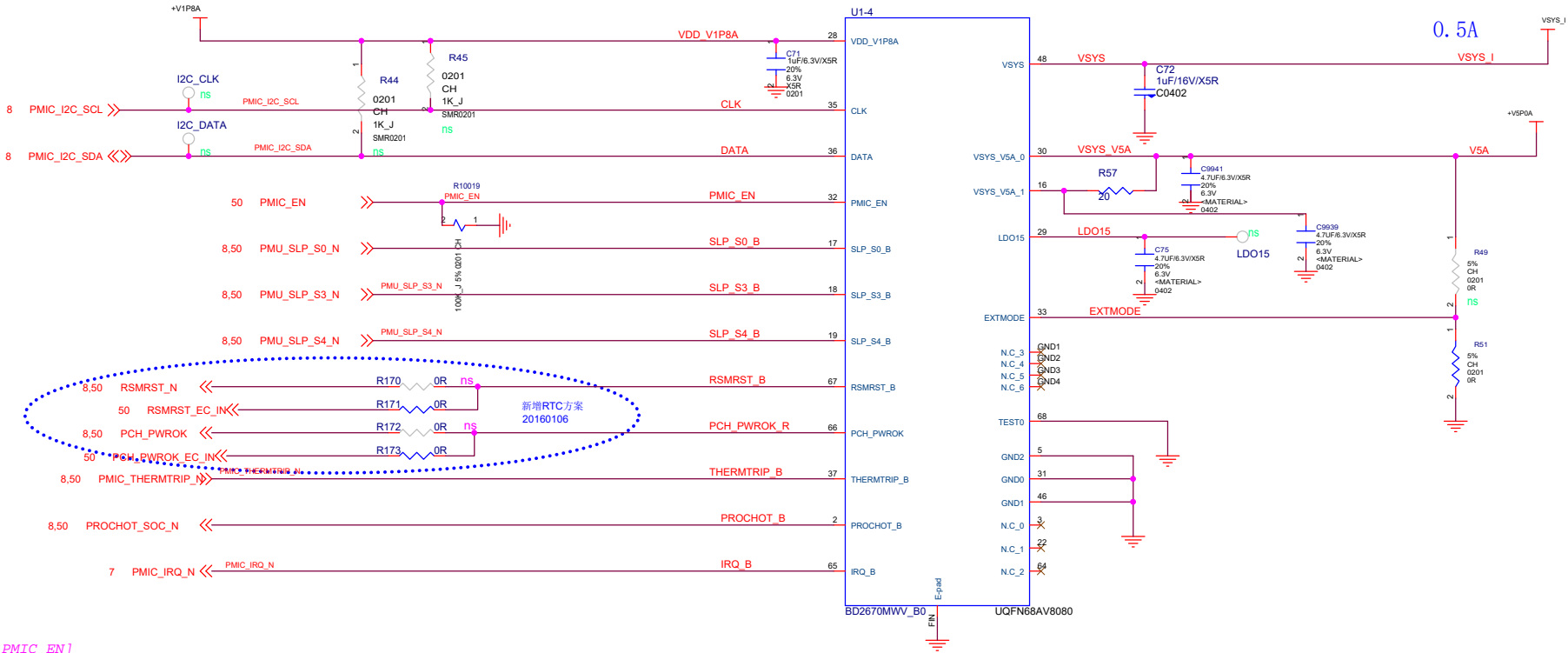


V1P8A[BUCK4], V1P24A[BUCK5],VDDQ[BUCK6]



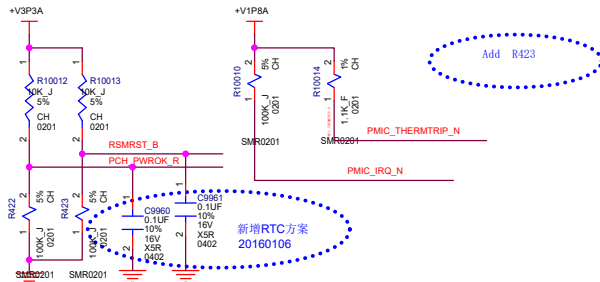
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GATE1	Sequence Control	SLP_S3_B Control	Sequence Control	SLP_S3_B Control
VDDQ	1.2V	1.35V	1.1V	1.1V


I2C , Other signals



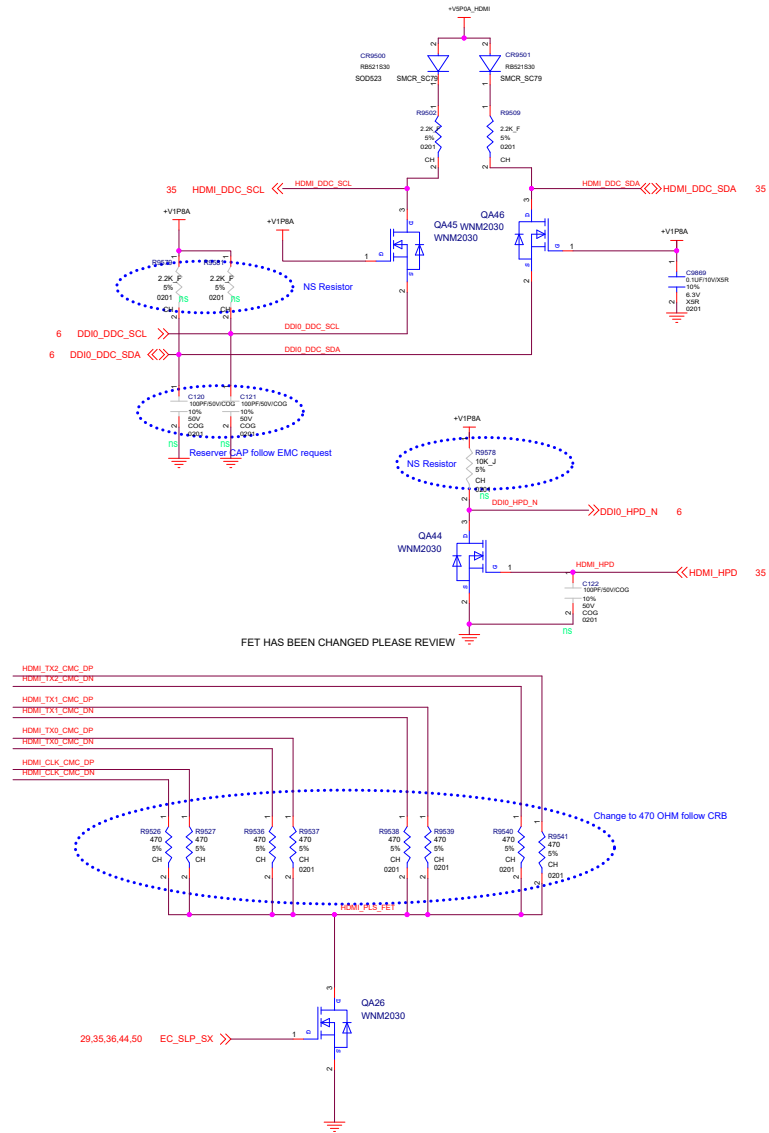
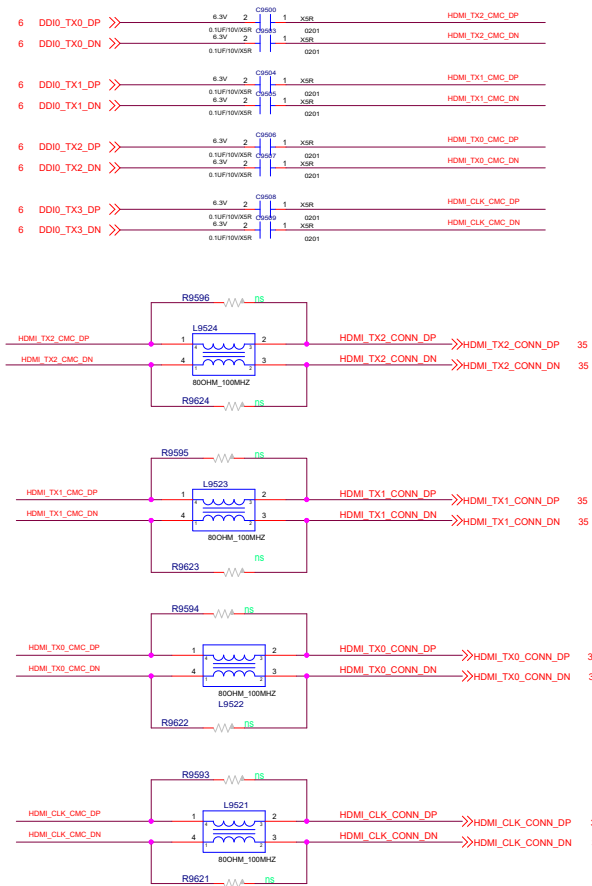
```
[PMIC_EN]
Connected to EC.
```

[PCH_FWROK, RSMRST_B, PROCHOT_B, IRQ_B]
These pins are open drain outputs and
need pull-up resistors according to the condition of the connected devices.

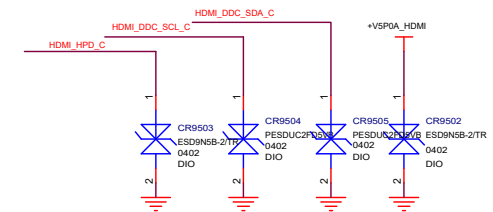
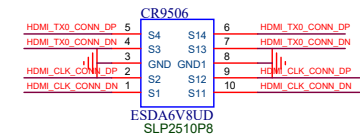
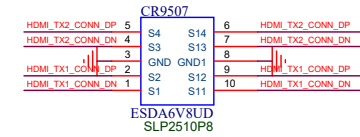
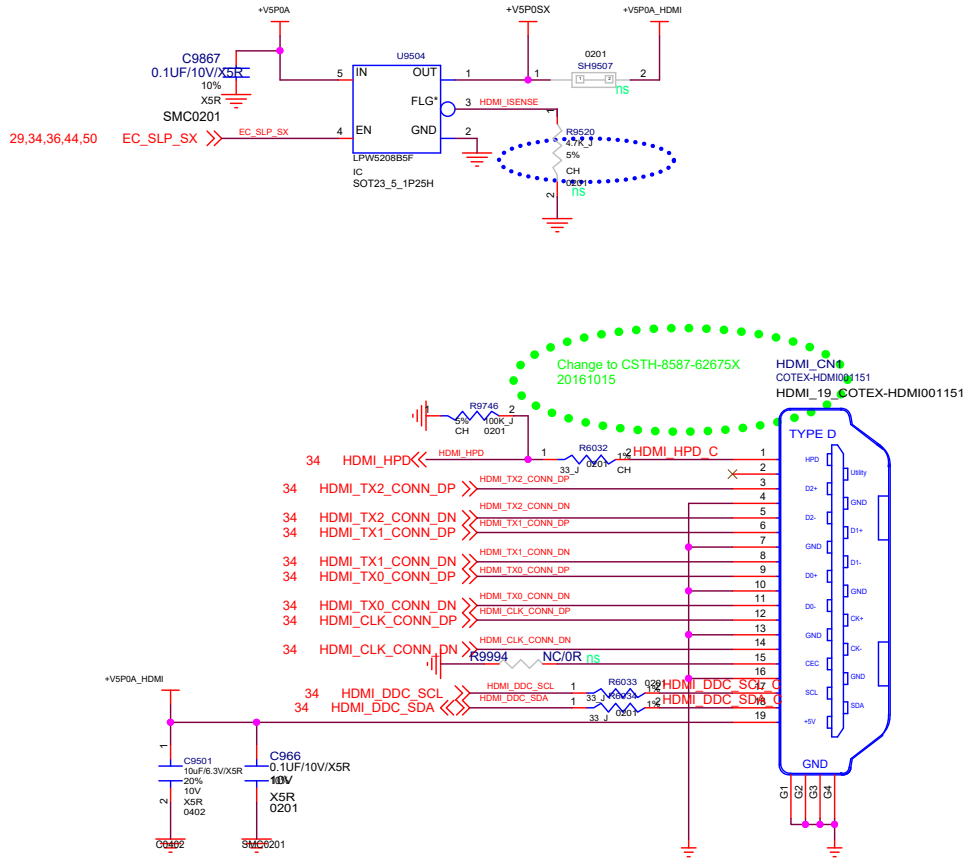


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Title			
APOLLO LAKE			
Size	Document Number	Rev	
Custom	POWER DELIVERY1.8V AND 1.0V	R100	
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HDMI LEVEL SHIFTERS & CONNECTOR



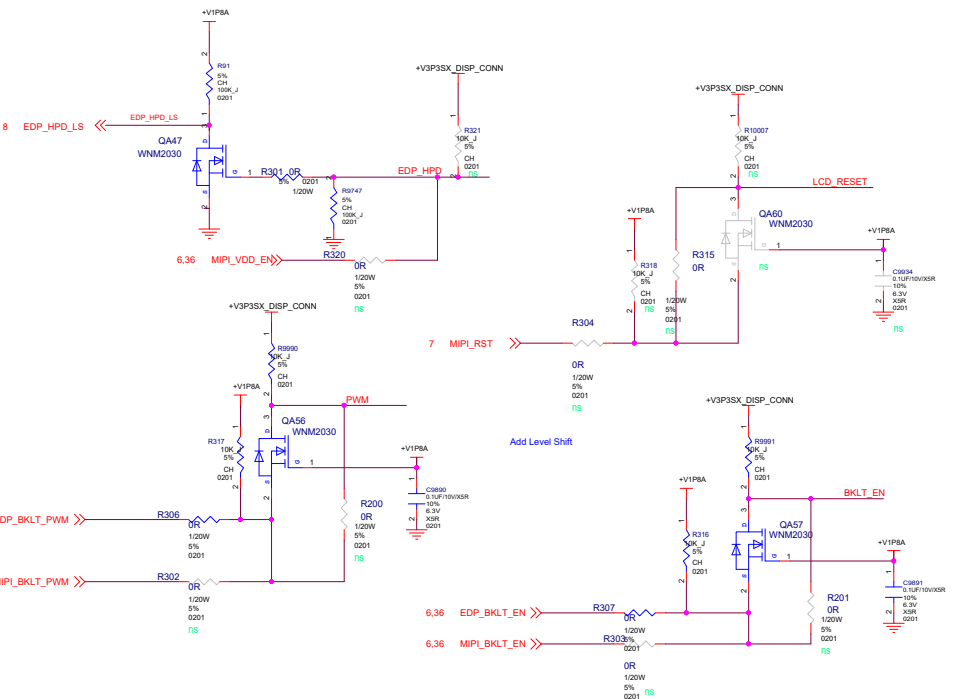
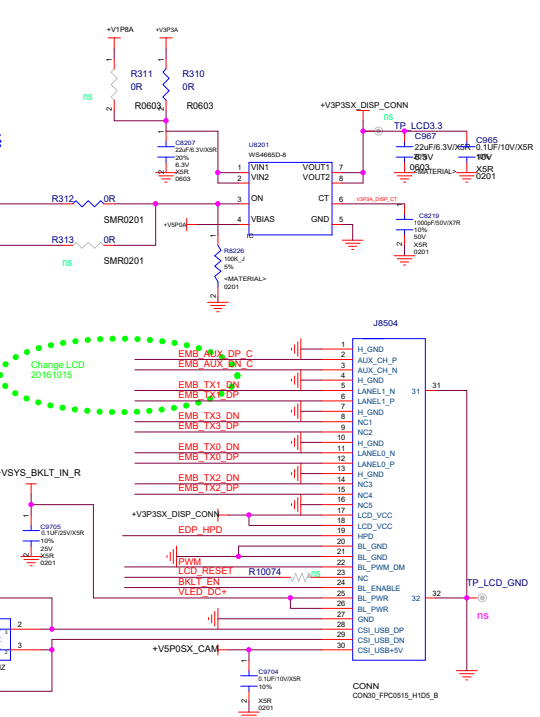
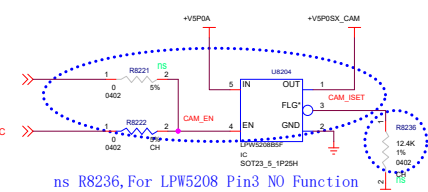
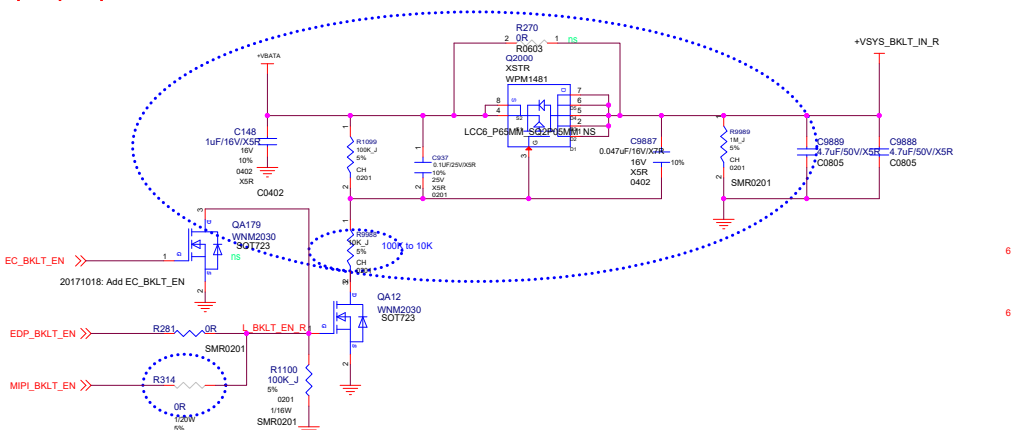
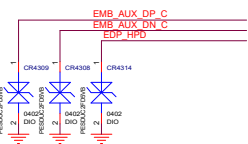
ns R8236,For LPW8208 pin 3 NO Function

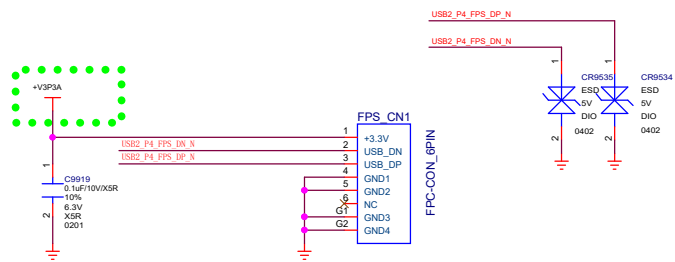
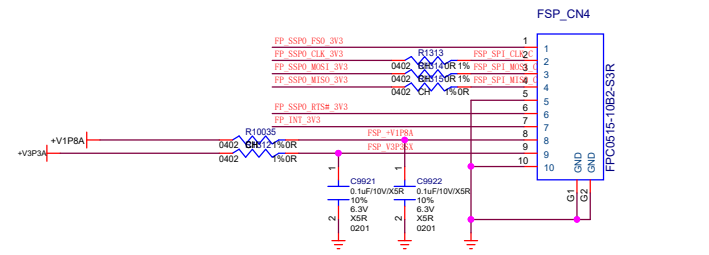


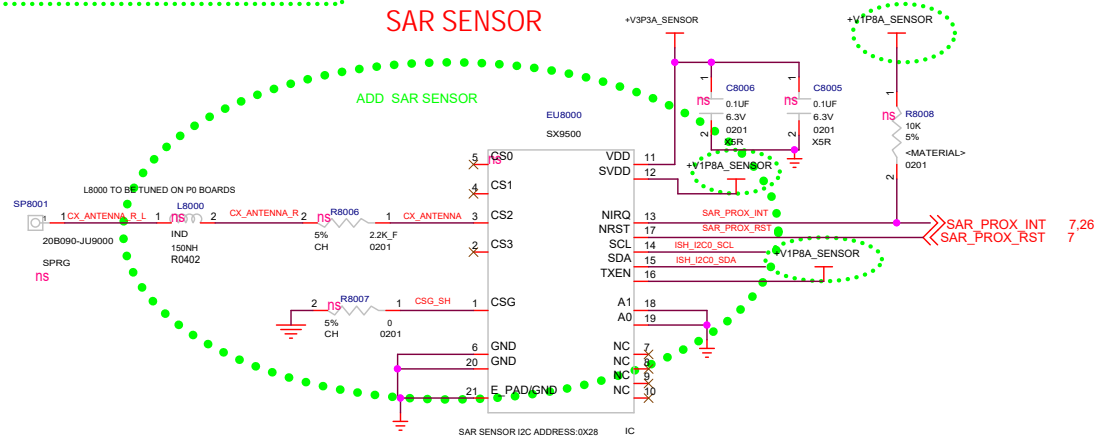
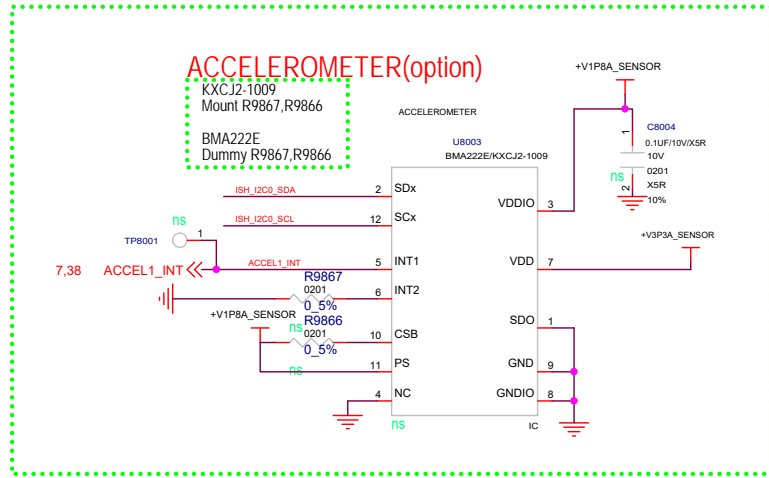
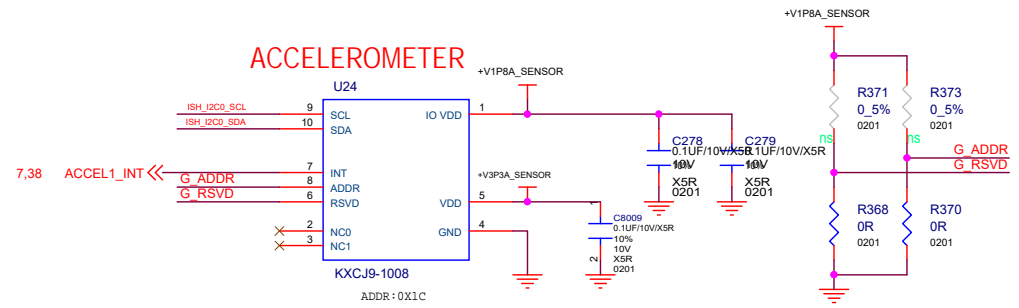
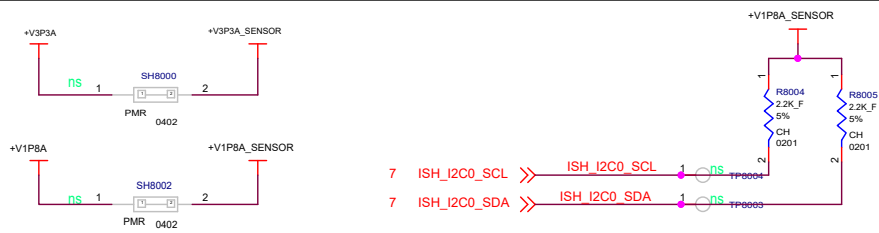
T65 0.5 EDP HPD
T66 0.5 EMB AUX DP C
T67 0.5 EMB AUX DN C
T72 0.5 EMB TX1 DN
T73 0.5 EMB TX1 DP
T74 0.5 EMB TX0 DN
T75 0.5 EMB TX0 DP


D42
EMB TX3 DP 5 EMB TX3 DP
EMB TX3 DN 4 EMB TX3 DN
EMB TX1 DP 3 EMB TX1 DP
EMB TX1 DN 2 EMB TX1 DN
ESDA6V8UD

D43
EMB TX2 DP 5 EMB TX2 DP
EMB TX2 DN 4 EMB TX2 DN
EMB TX0 DP 3 EMB TX0 DP
EMB TX0 DN 2 EMB TX0 DN
ESDA6V8UD







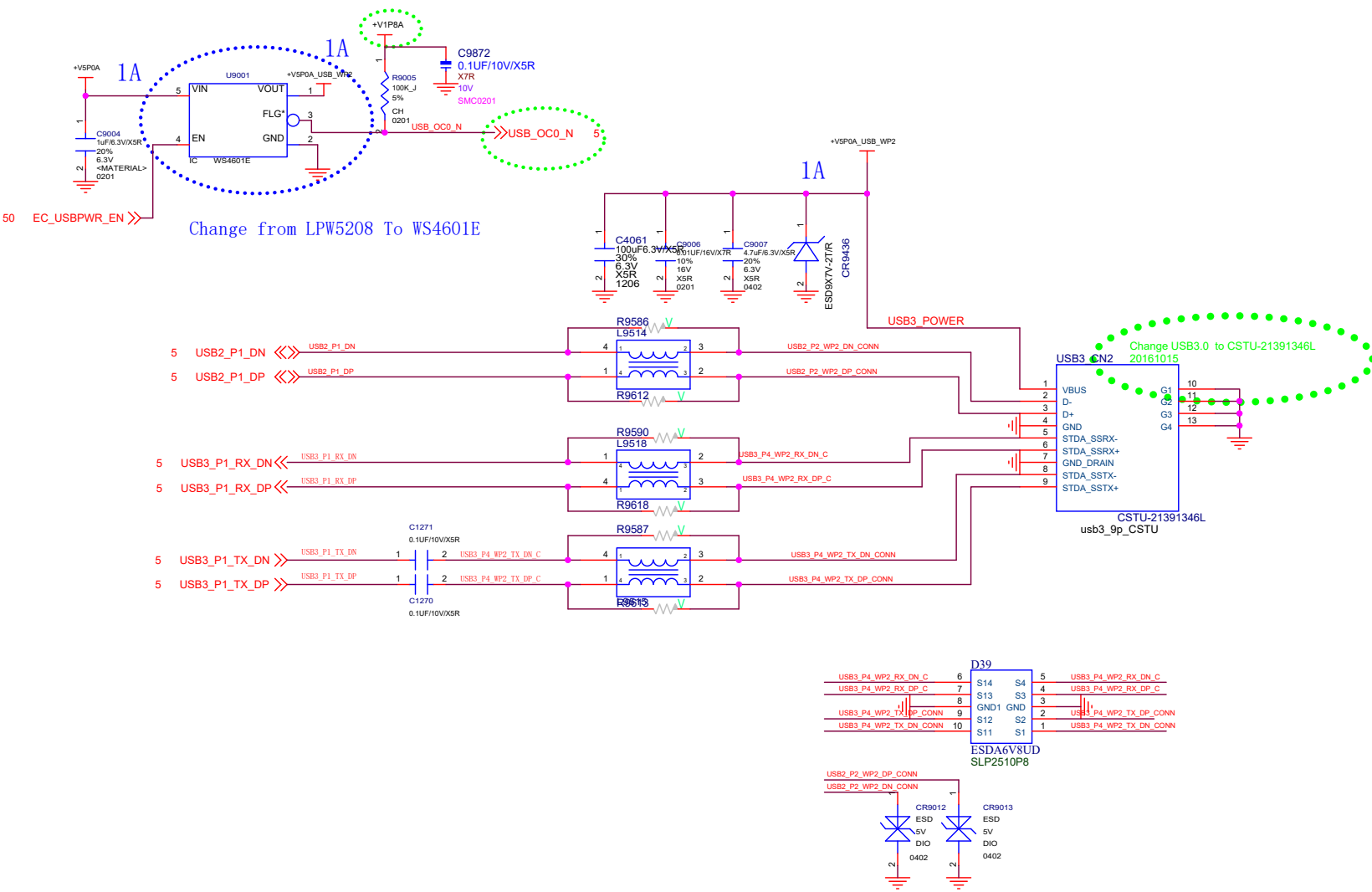


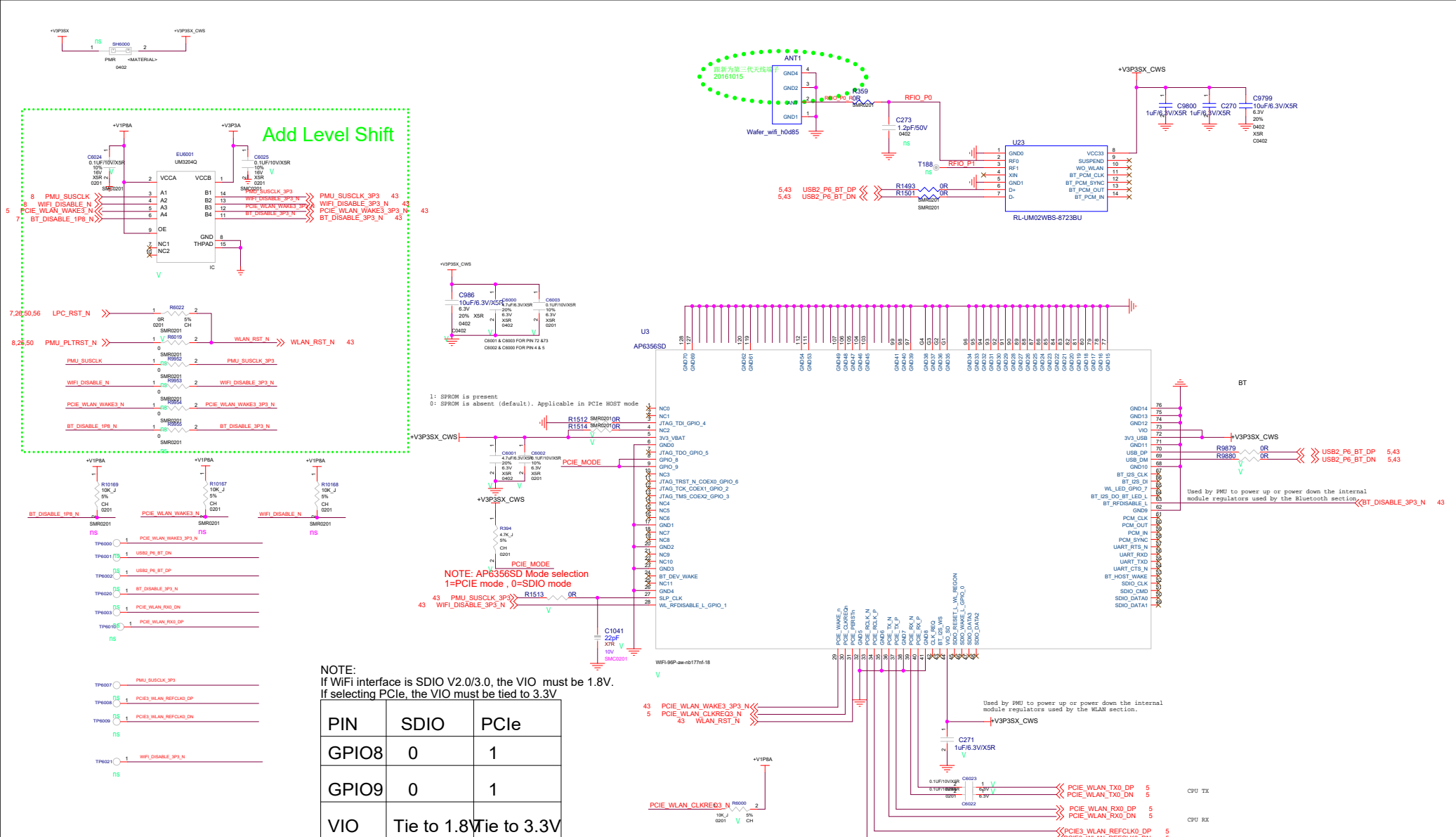
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APOLLO LAKE		
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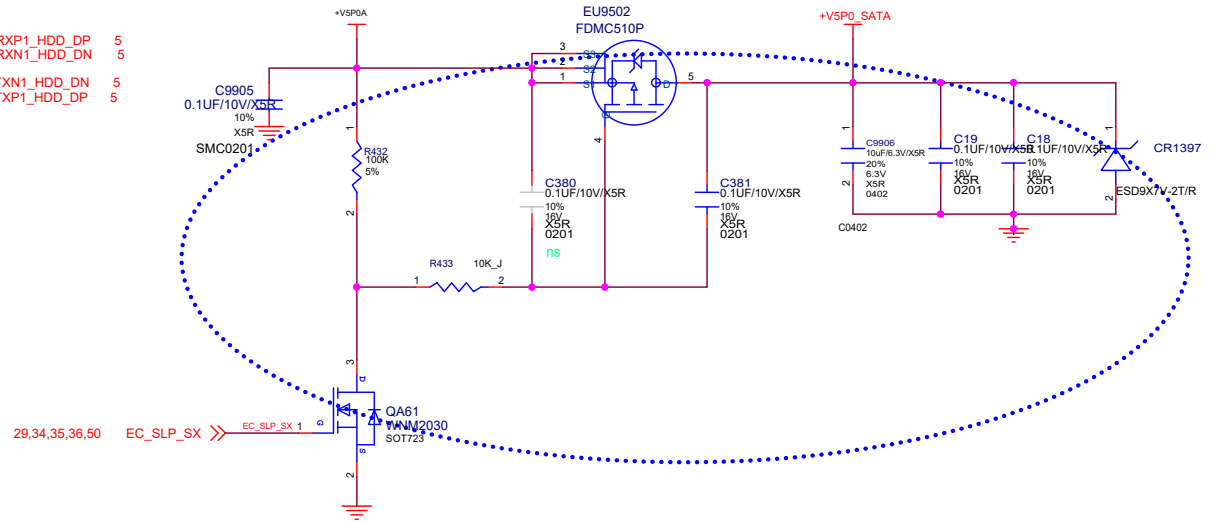
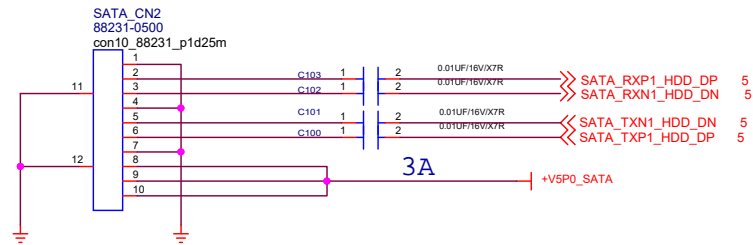



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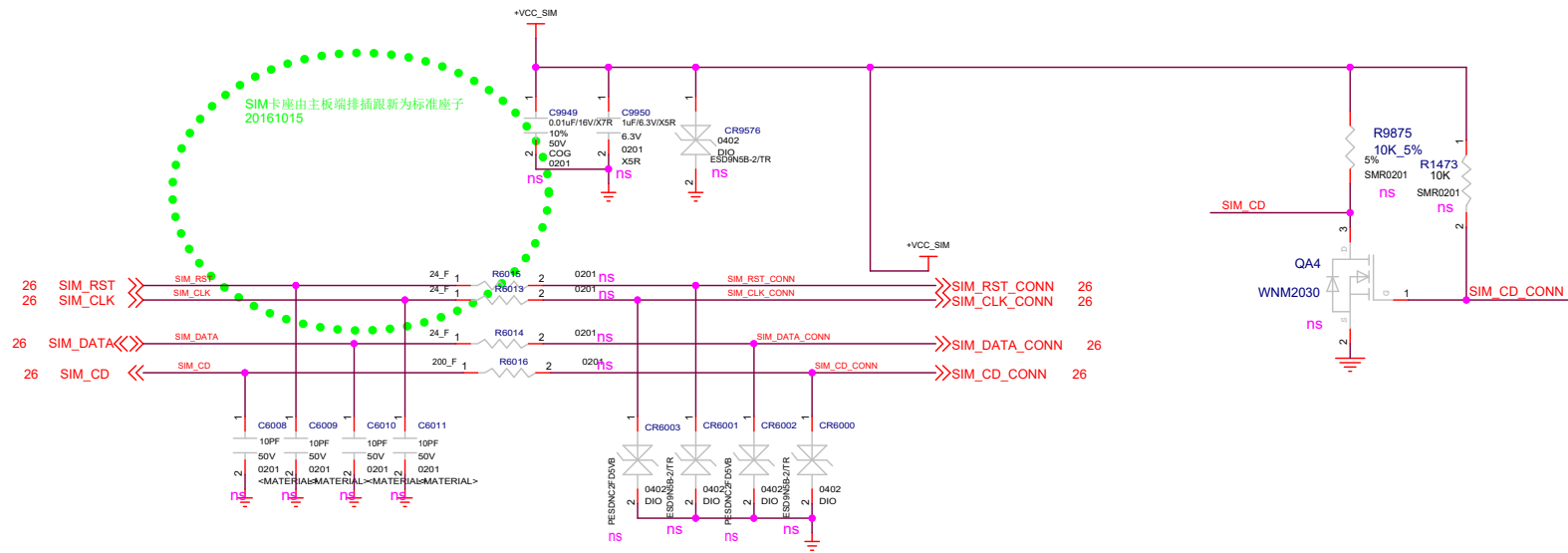
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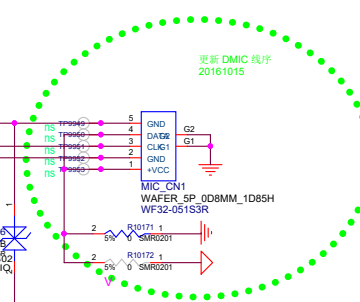
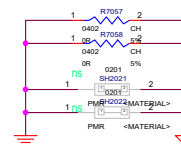


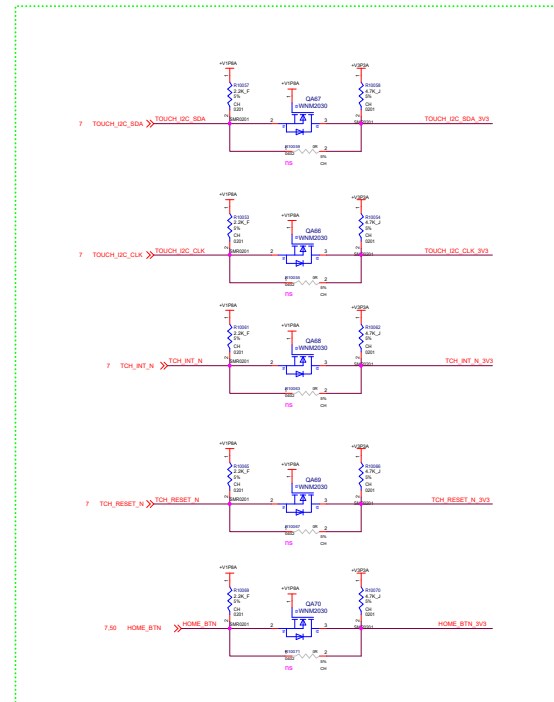
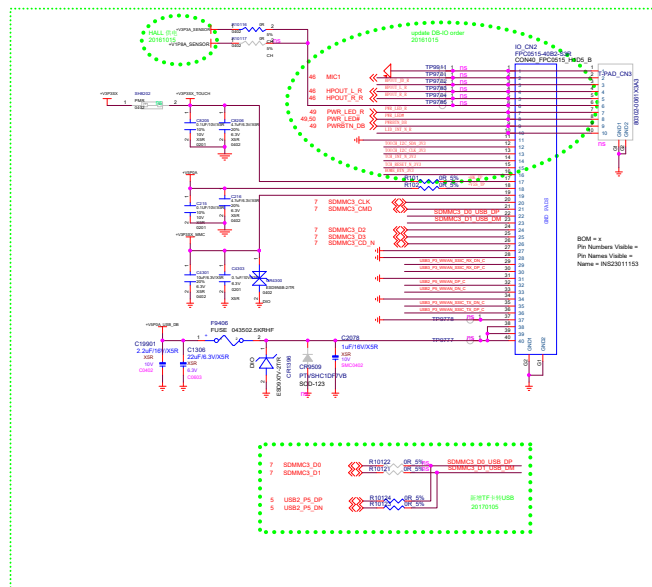
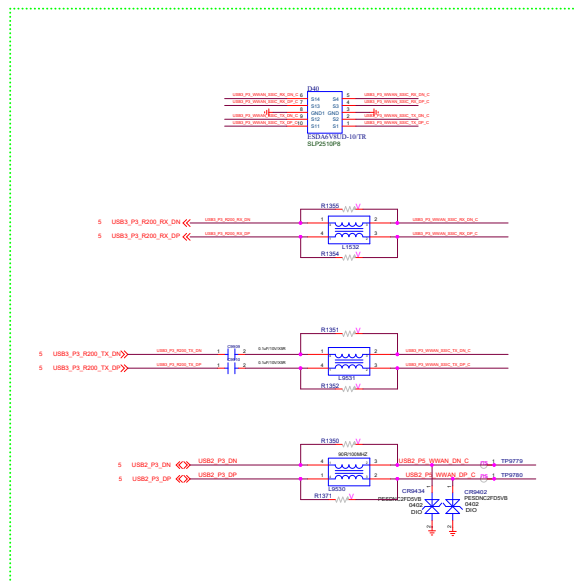
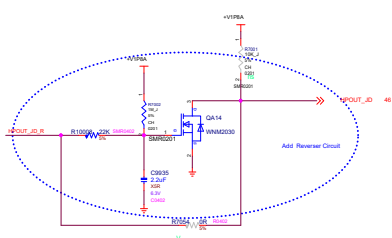
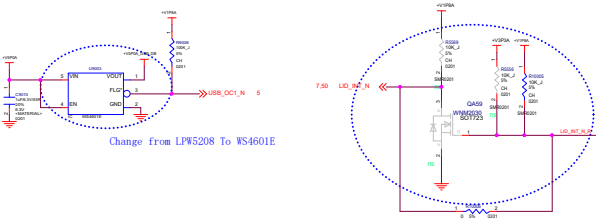
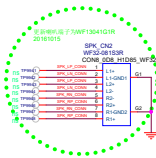
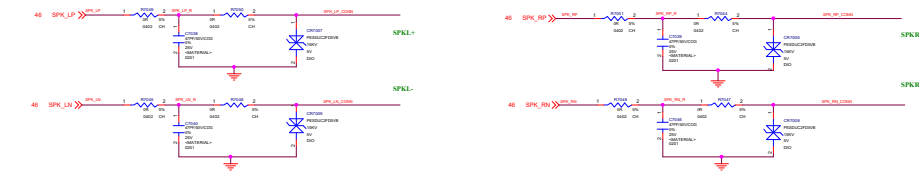


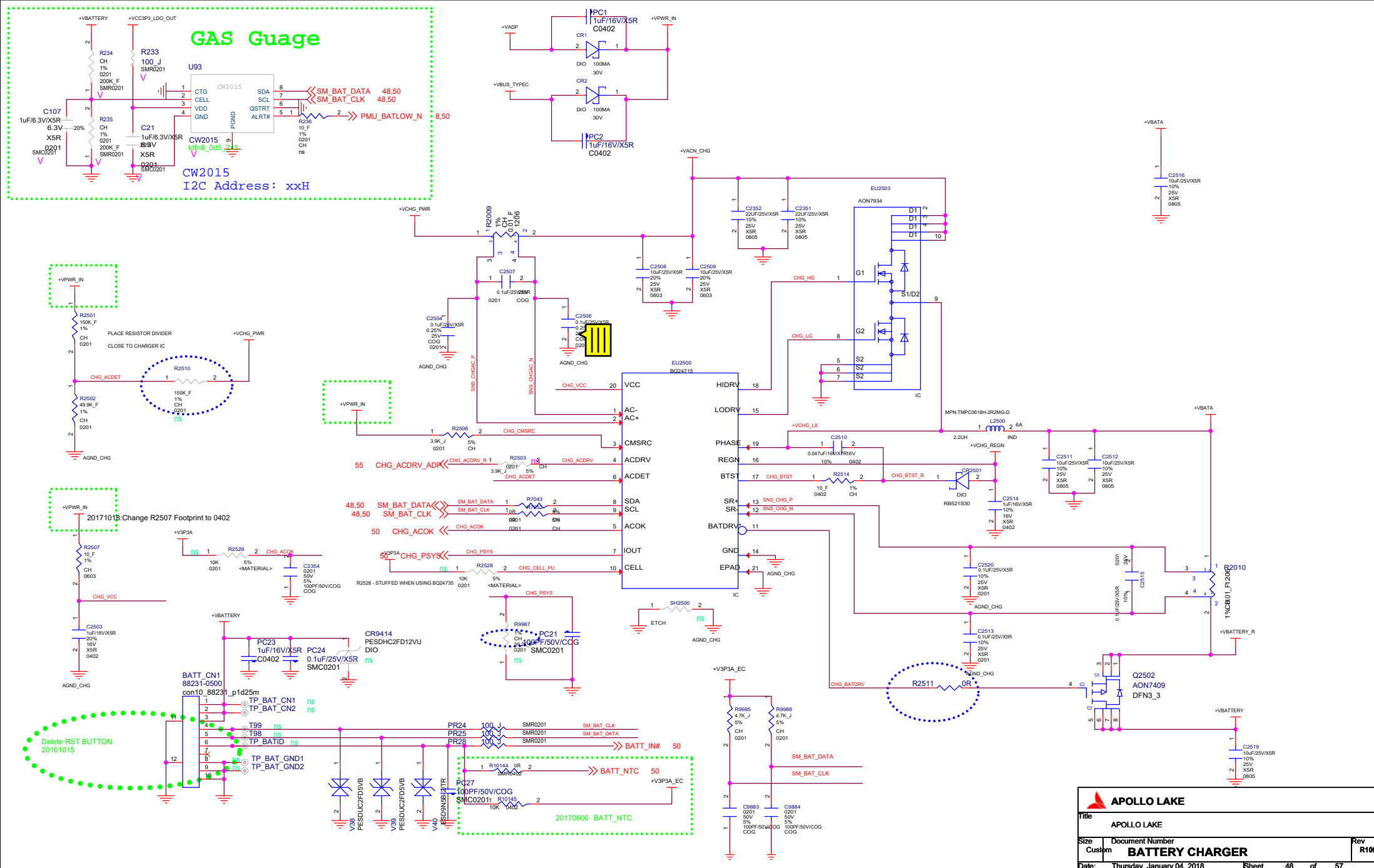
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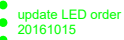


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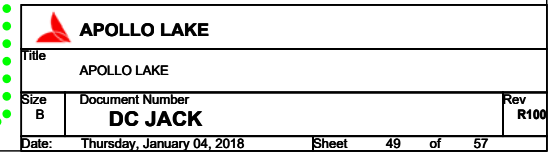
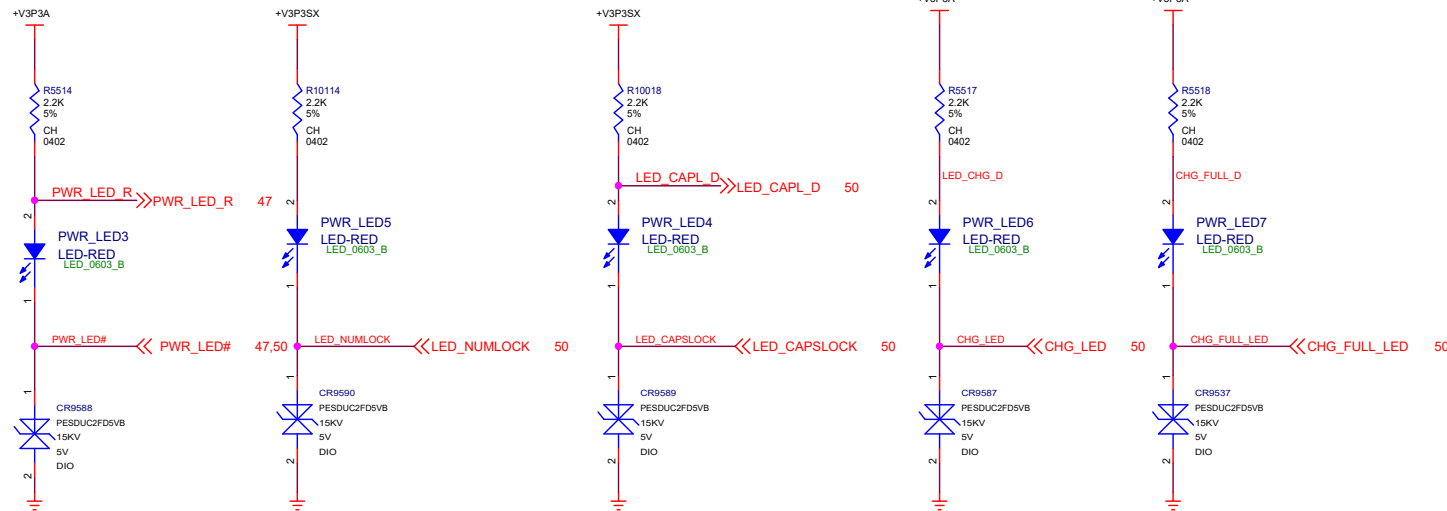
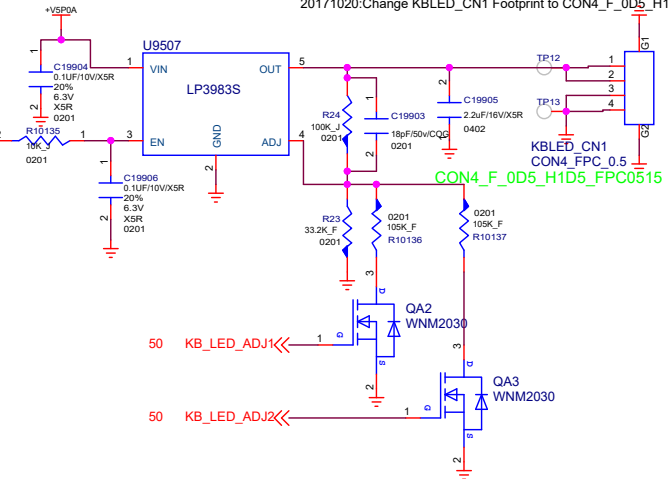



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KEYBOARD BACKLIGHT

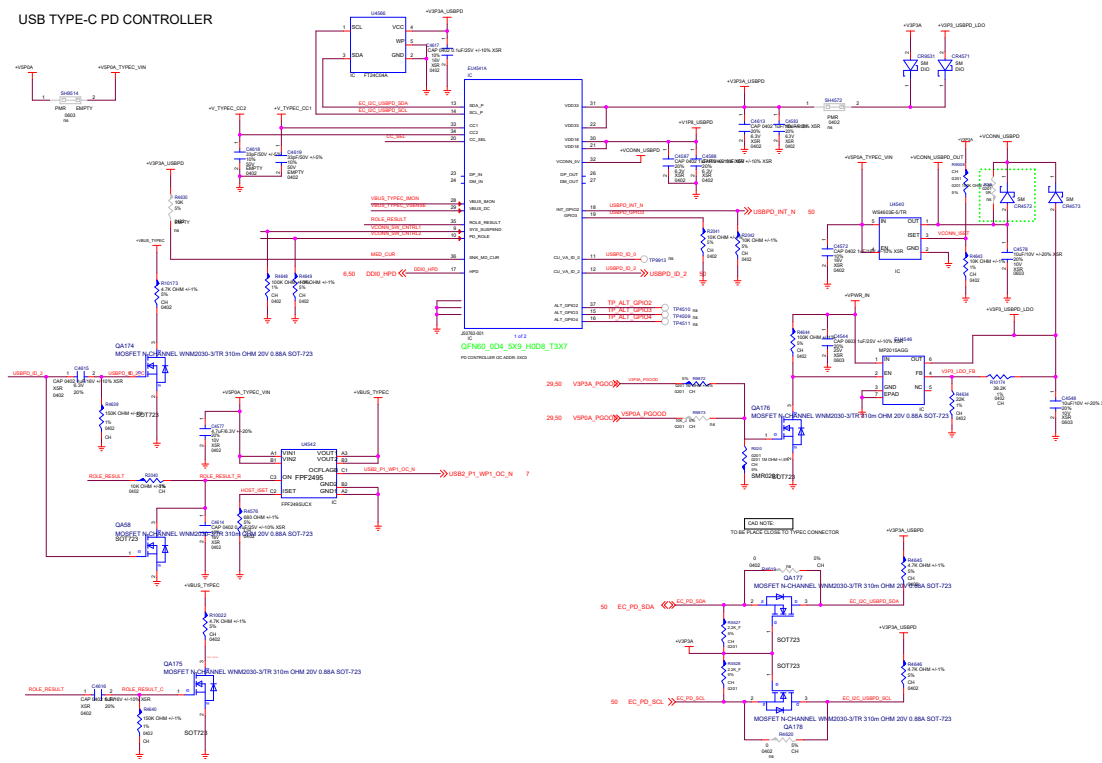
20171020:Change KBLED_CN1 Footprint to CON4_F_0D5_H1D5_FPC0515



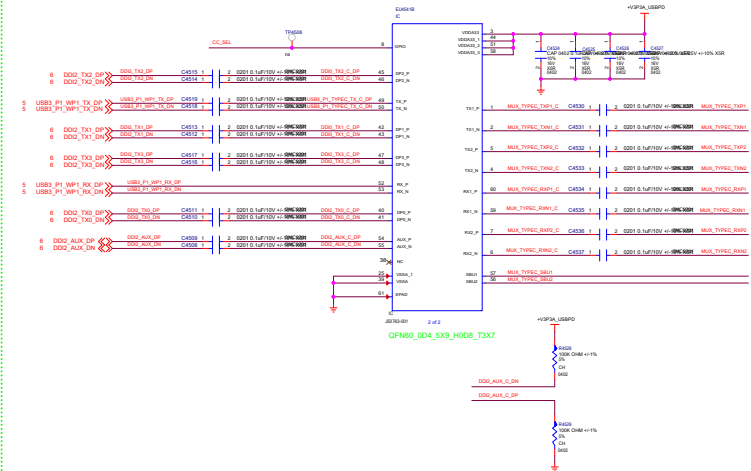
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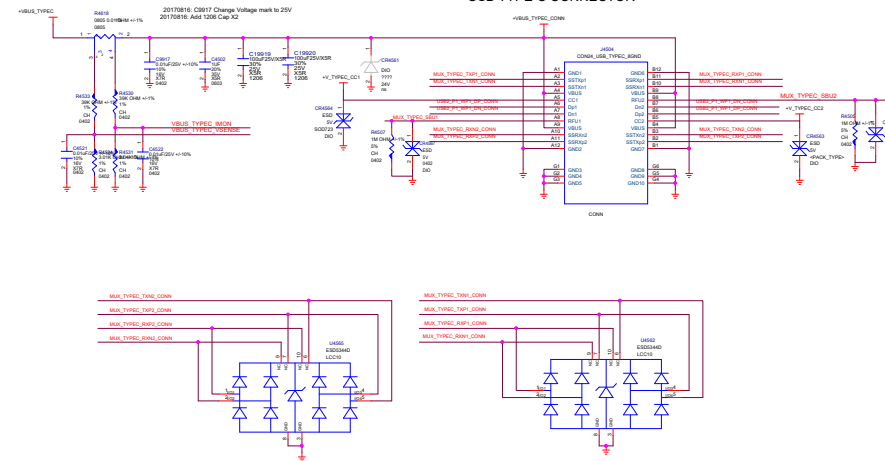
USB TYPE-C PD CONTROLLER

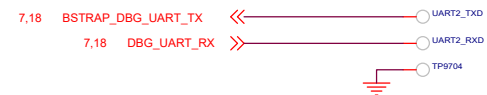
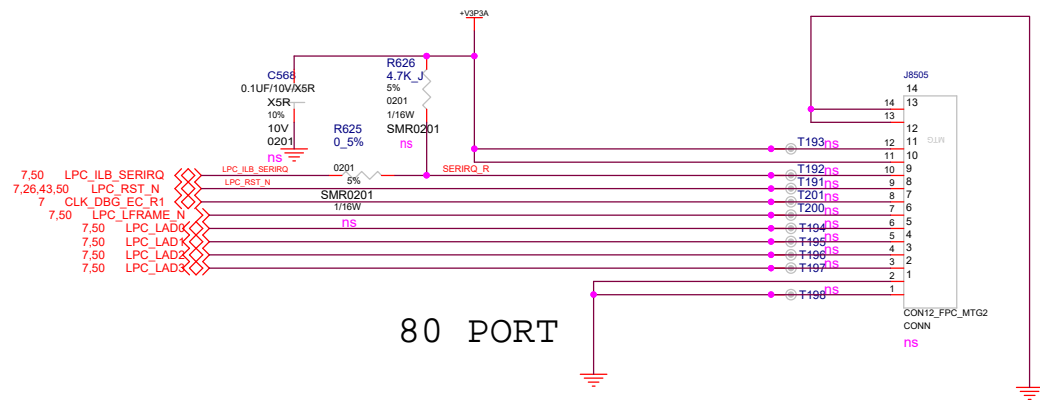


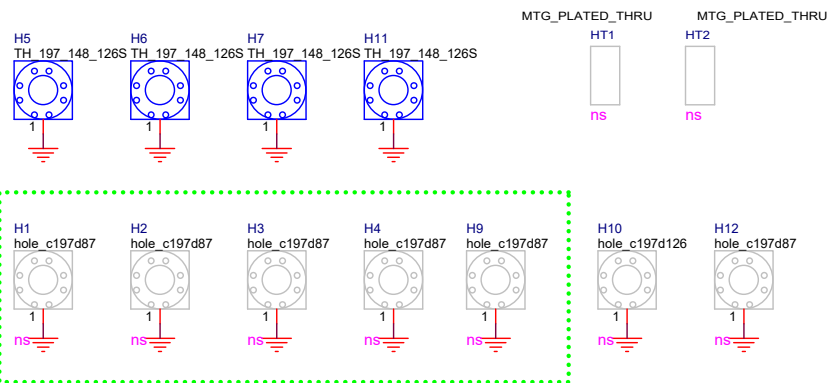
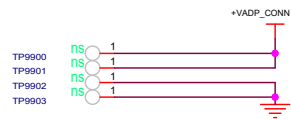
USB TYPE-C MUX




USB TYPE-C CONNECTOR







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